

S-SERIES 2009

INVICTA (Intel-Discrete)

MV Build (A03) - Final

2009.03.18

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC TITLE <i>INVICTA Cycle1</i>
DESIGN					
CHECK					
RESPONSIBLE					
SIZE =	3	VER :			SIZE CODE DOC. NUMBER REV
FILE NAME :	XXXX-XXXXXX-XX				A3 CS J310A22527-0-MTR A03
P/N	XXXXXXXXXXXX				SHEET 1 OF 54

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8- System Power (+V1.8/+VCCP) .	18- CPU Penryn-3.	39- KBC, BIOS ROM, Debug Port.
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	31- S/B ICH9-1.	52- AMD M92S2-4.
	32- S/B ICH9-2.	53- Video RAM-1 (DDR2).
	33- S/B ICH9-3.	54- Video RAM-2 (DDR2).
	34- S/B ICH9-4.	
	35- S/B ICH9-5.	

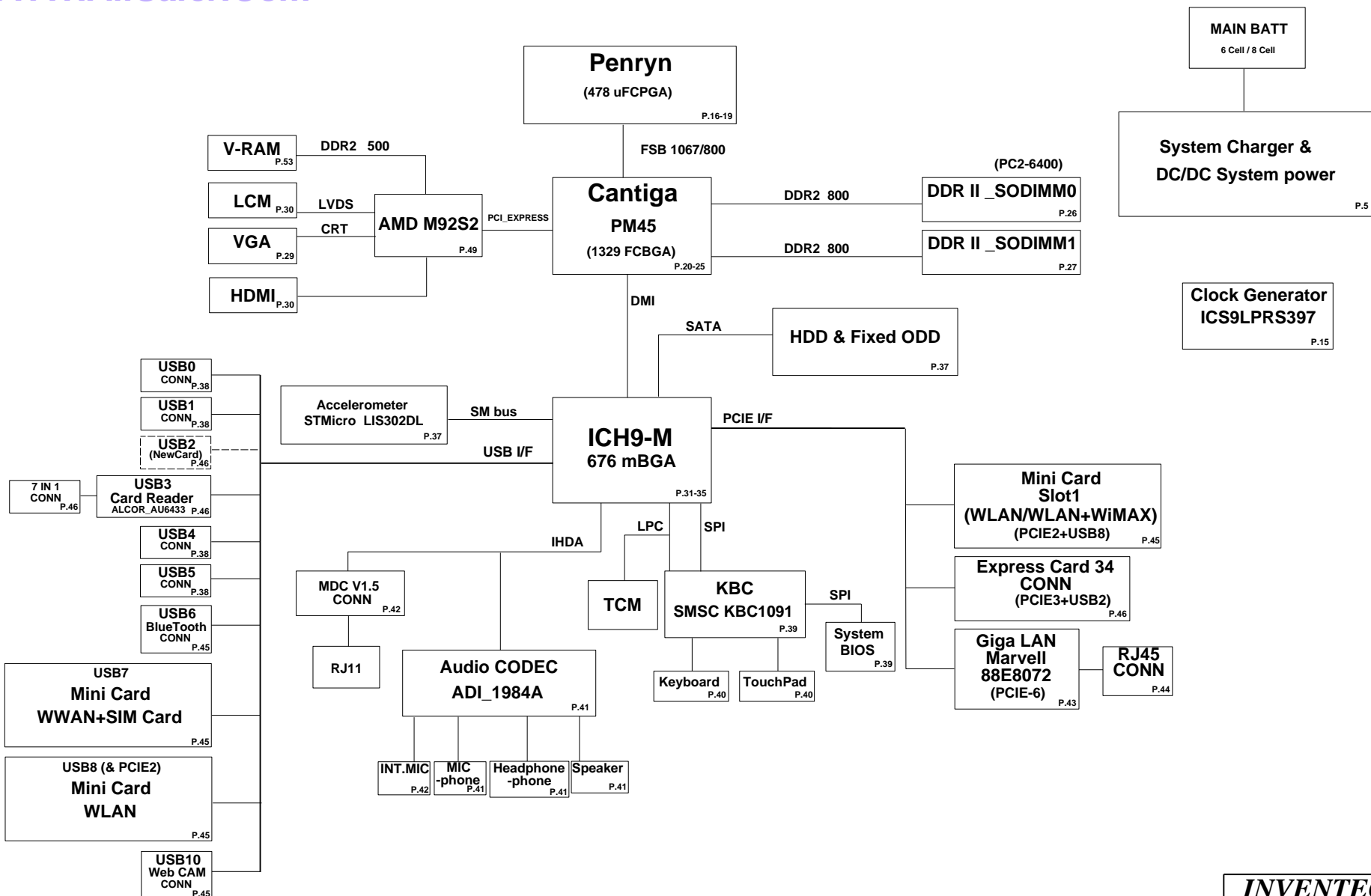
INVENTEC

TITLE **INVICTA Cycle1**
Contents

SIZE A3	CODE CS	DOC. NUMBER 1310A22527-0-MTR	REV A03
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CHANGE by *Drawer Name* 17-Mar-2009

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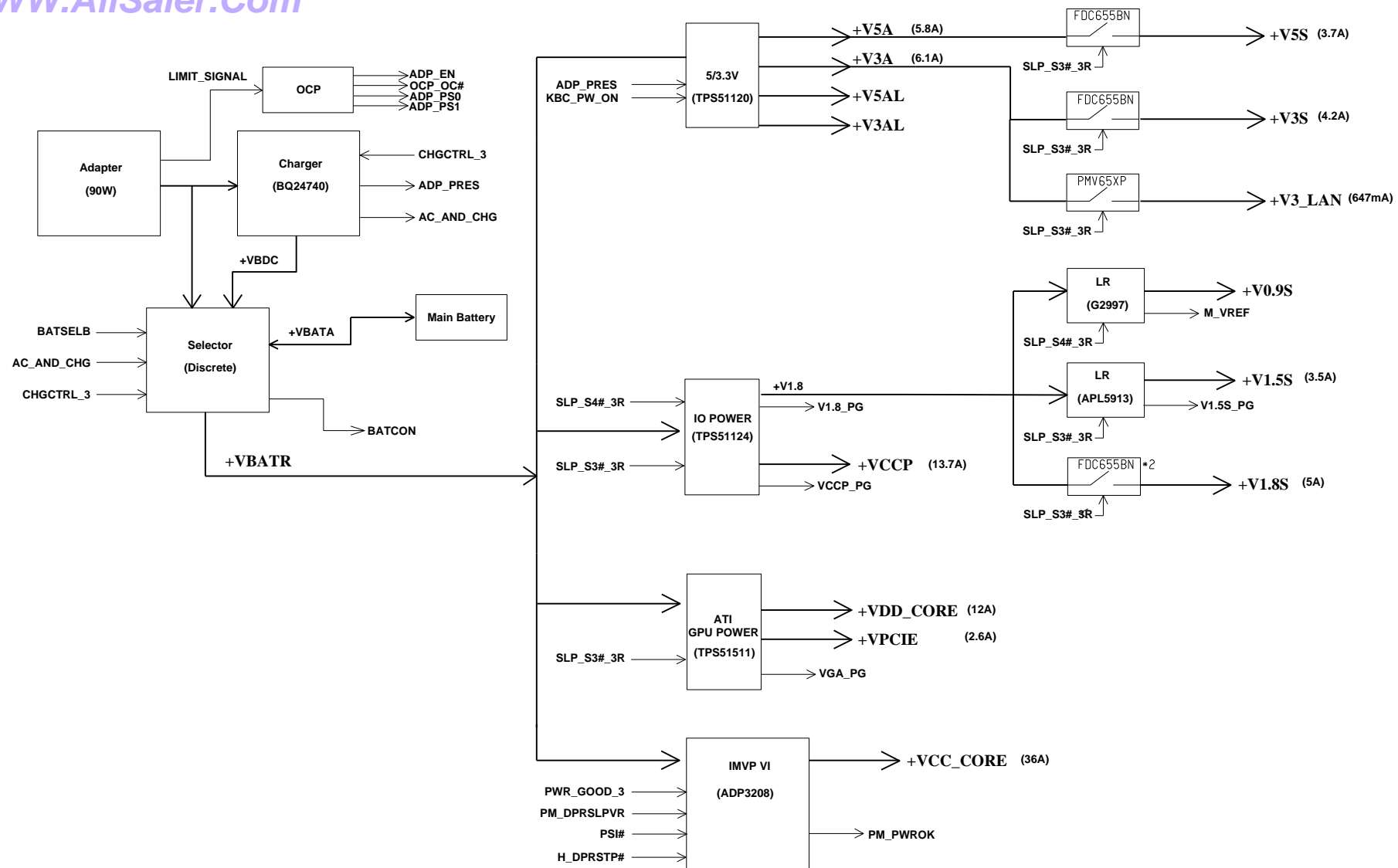


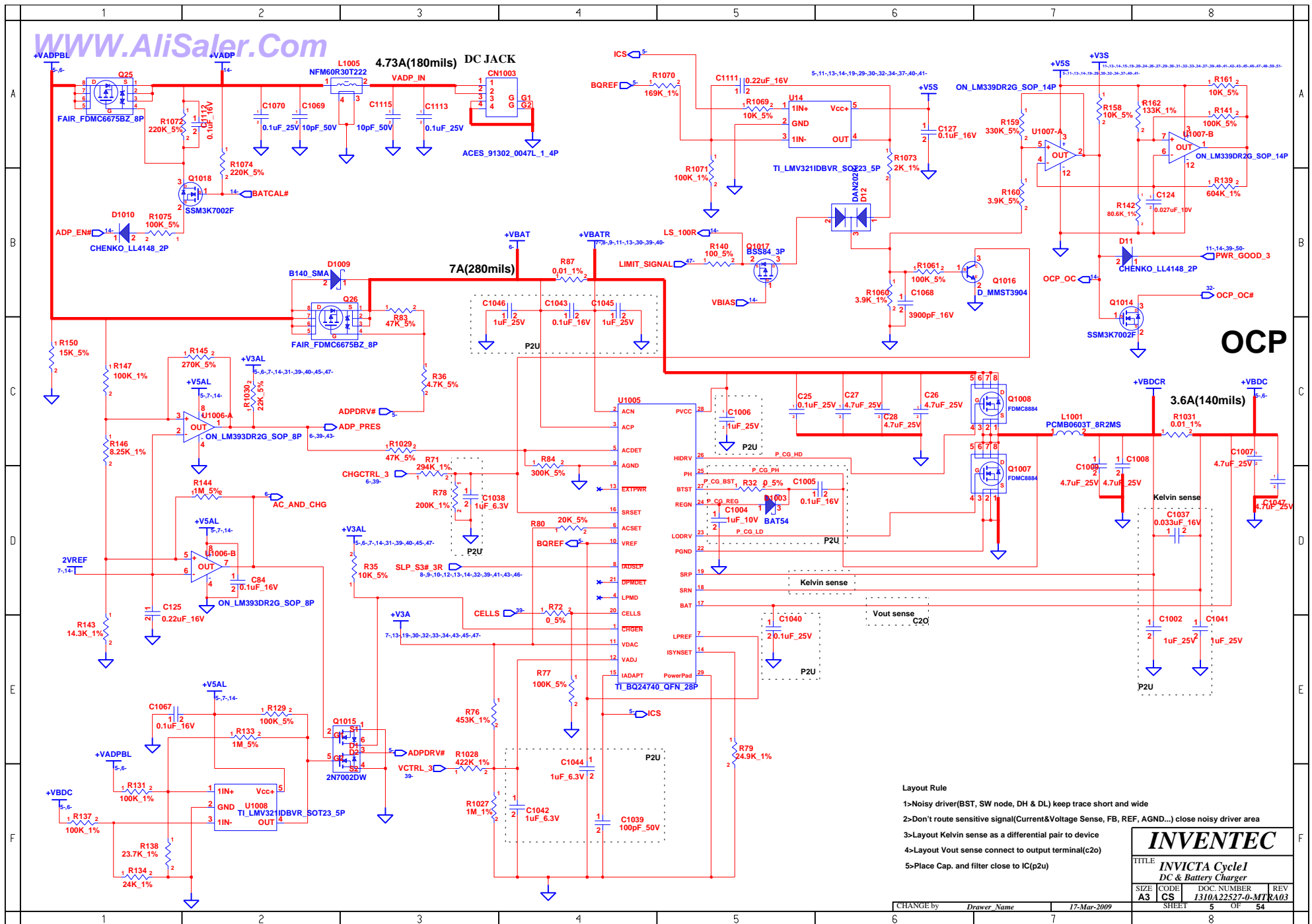
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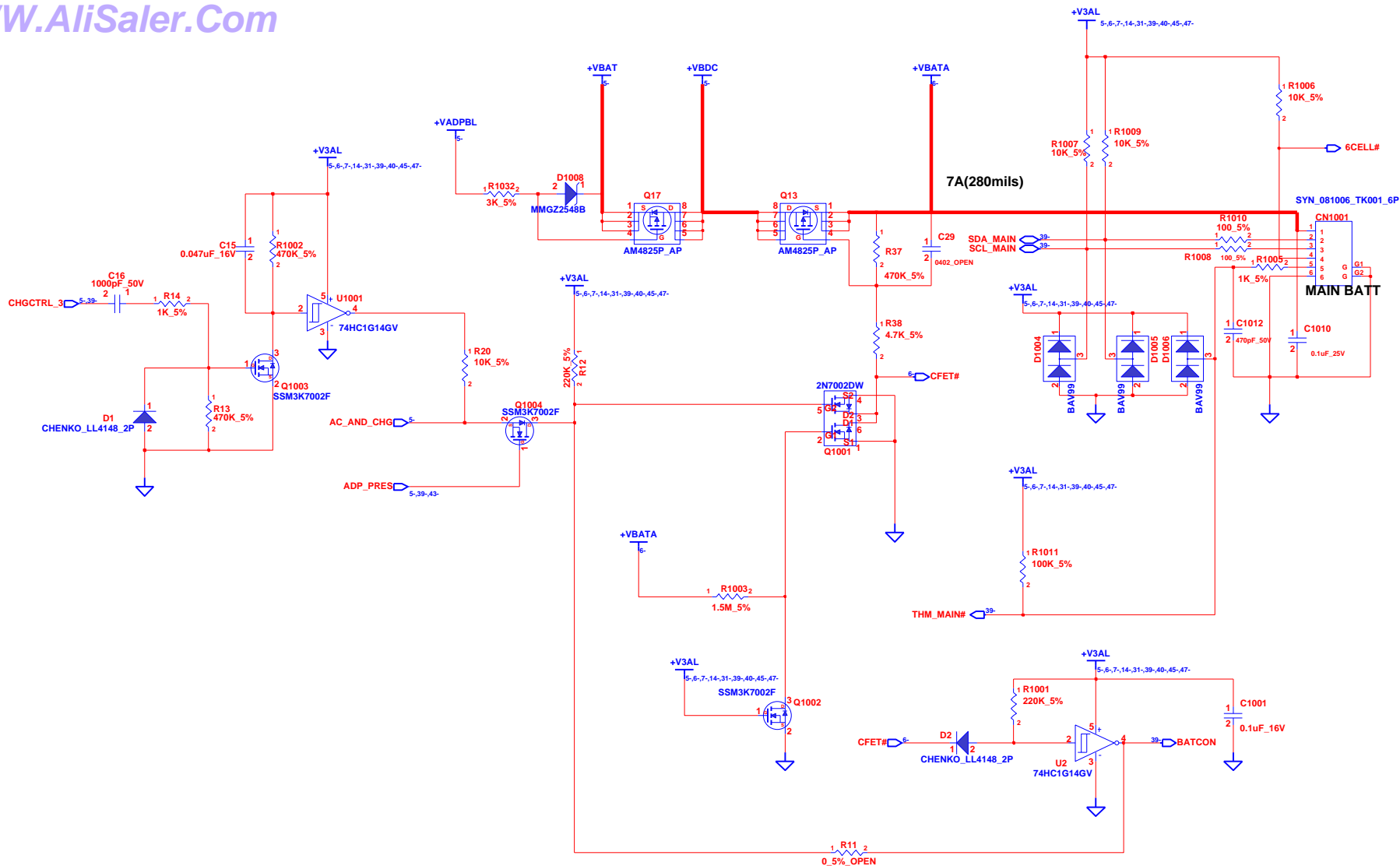
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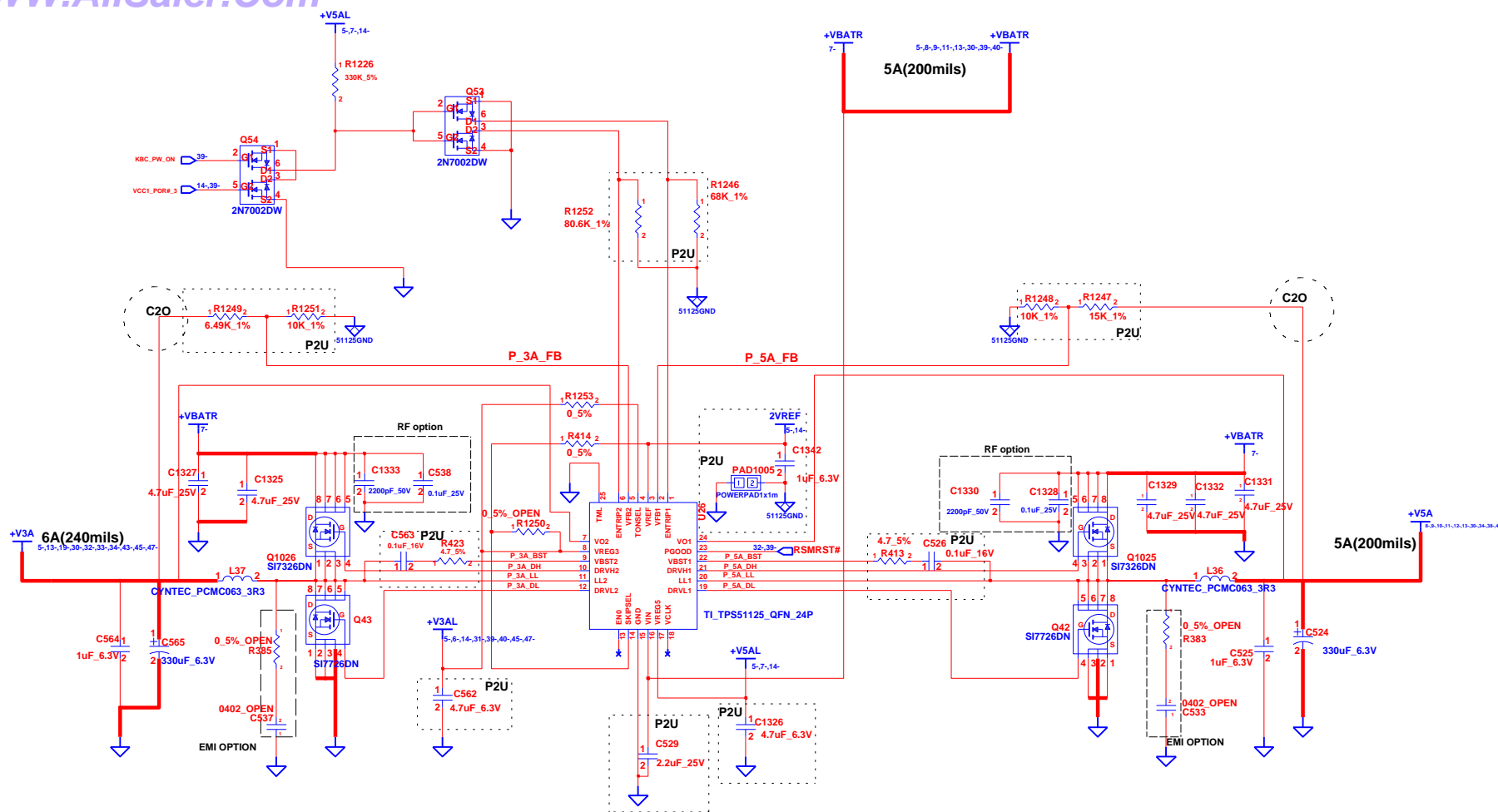
CHANGE by *Drawer Name* 17-Mar-2009







INVENTEC			
TITLE INVICTA Cycle1			
Selector & Battery CONN & Extend/B			
SIZE A3	CODE CS	DOC NUMBER 1310A22527-0-MT	REV RA03
CHANGE by		Drawer Name	17-Mar-2009
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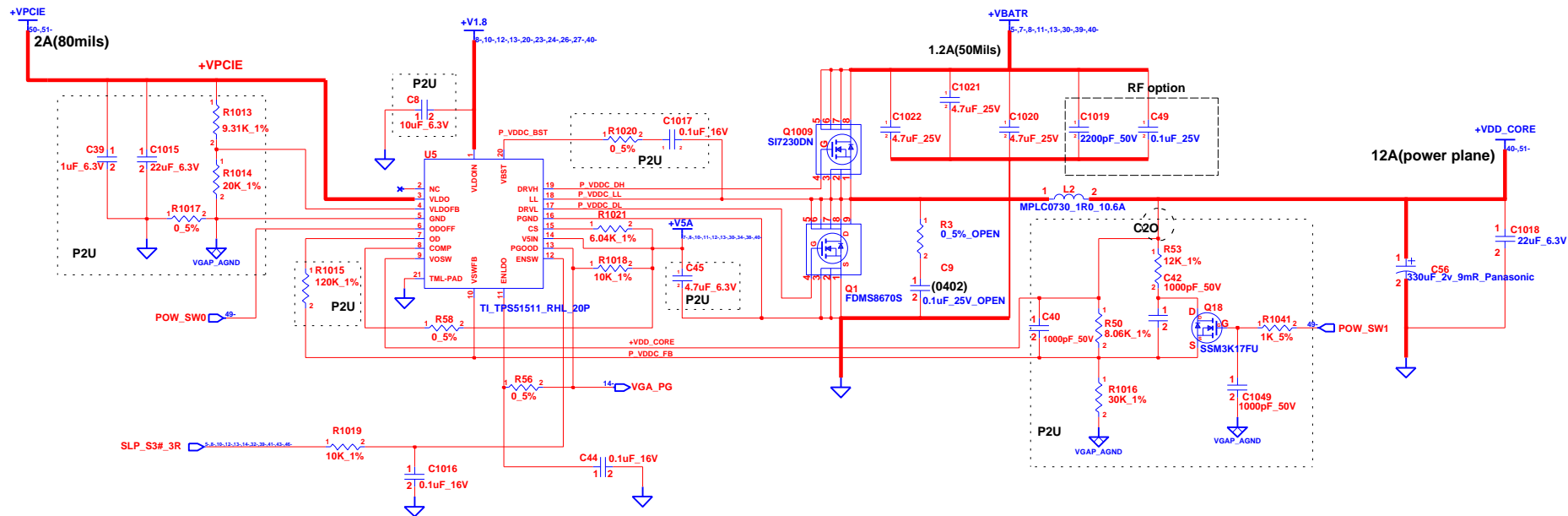
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TITLE INVICTA Cycle1 System Power(+V5A/+V3A)			
SIZE A3	CODE CS	DOC. NUMBER I310A22527-0-MTR	REV A03

CHANGE by *Drawer Name* 17-Mar-2009

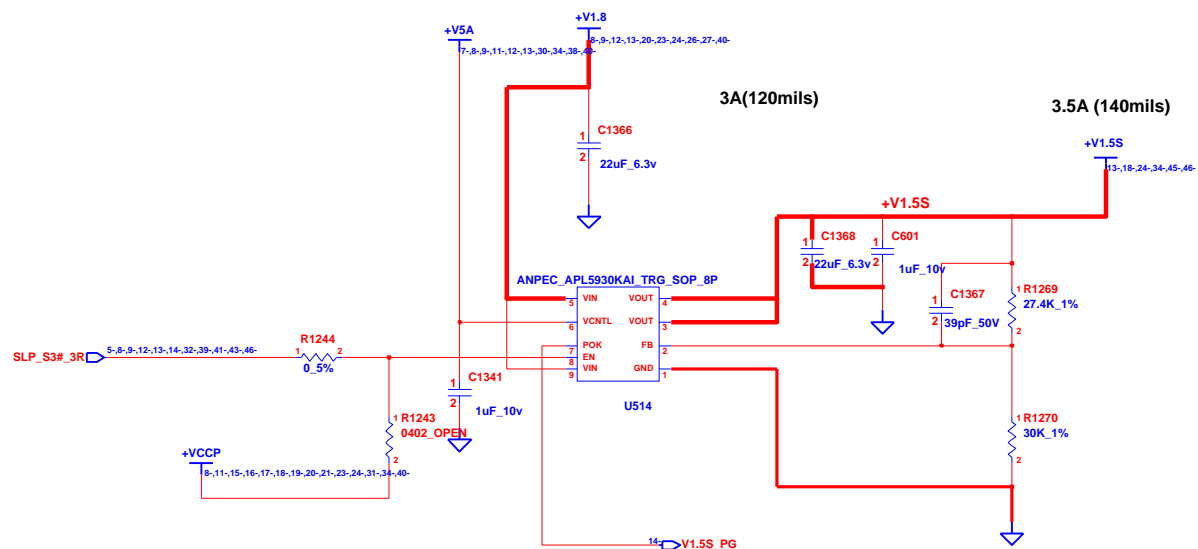
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POW_SW1	POW_SW0	+VDD_CORE
0	0	1.0V
0	1	0.95V
1	0	0.9V

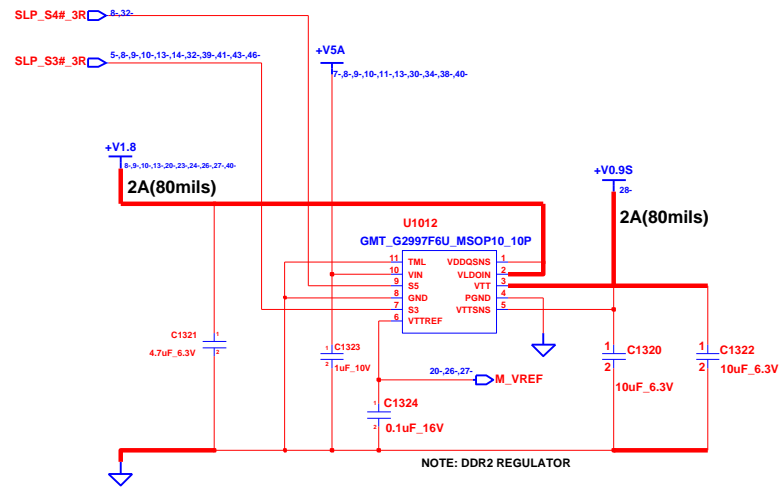
**INVENTEC**

TITLE	<i>INVICTA Cycle1</i> <i>Graphic Power (+VDD_CORE & +VPCIE)</i>
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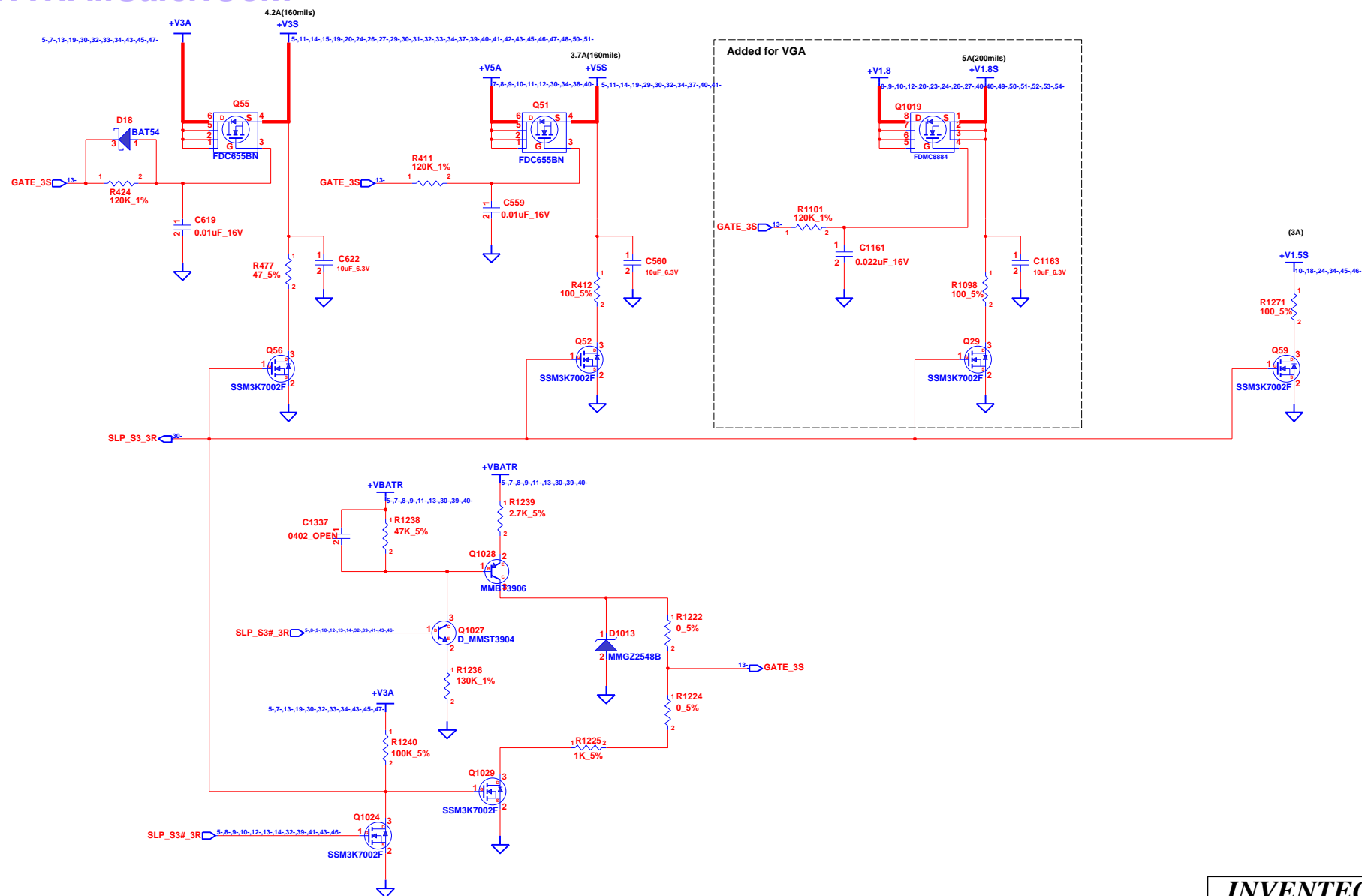
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INVENTEC			
TITLE INVICTA Cycle1 System Power (+V1.5S)			
SIZE A3	CODE CS	DOC. NUMBER I310A22527-0-MTR	REV A03
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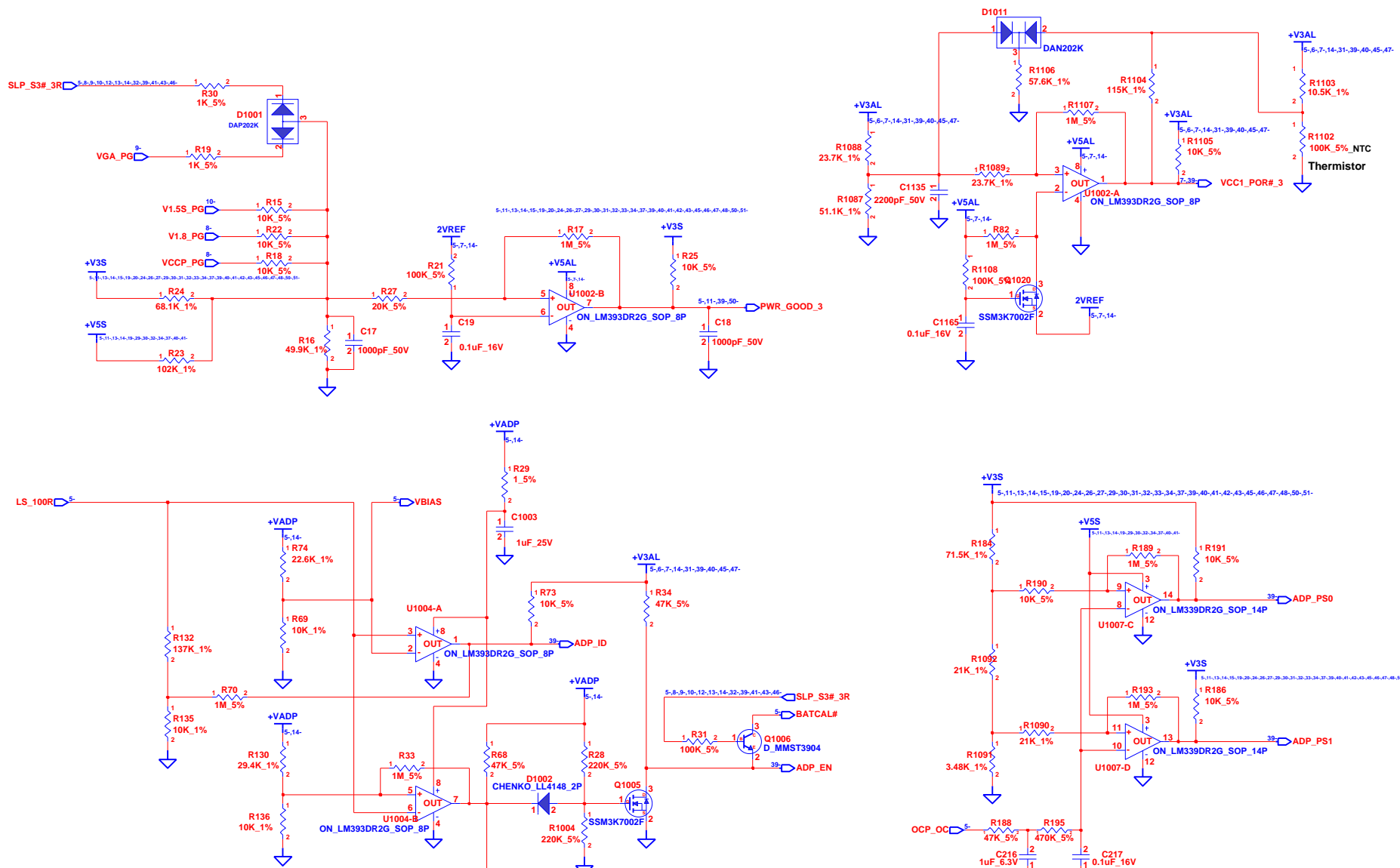
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TITLE INVICTA Cycle1			
DDR Termination Voltage (+V0.9S)			
SIZE A3	CODE CS	DOC. NUMBER 1310A22527-0-MTR	REV A03
CHANGE by Drawer Name		17-Mar-2009	
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INVENTEC

TITLE: INVICTA Cycle 1			
Power (Sleep): +VSS/+V3S/+V1.8S			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22527-0-MTR	REV: A03
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CHANGE by: Drawer Name 17-Mar-2009



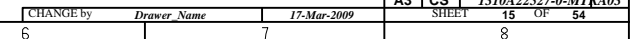
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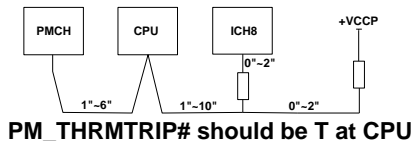
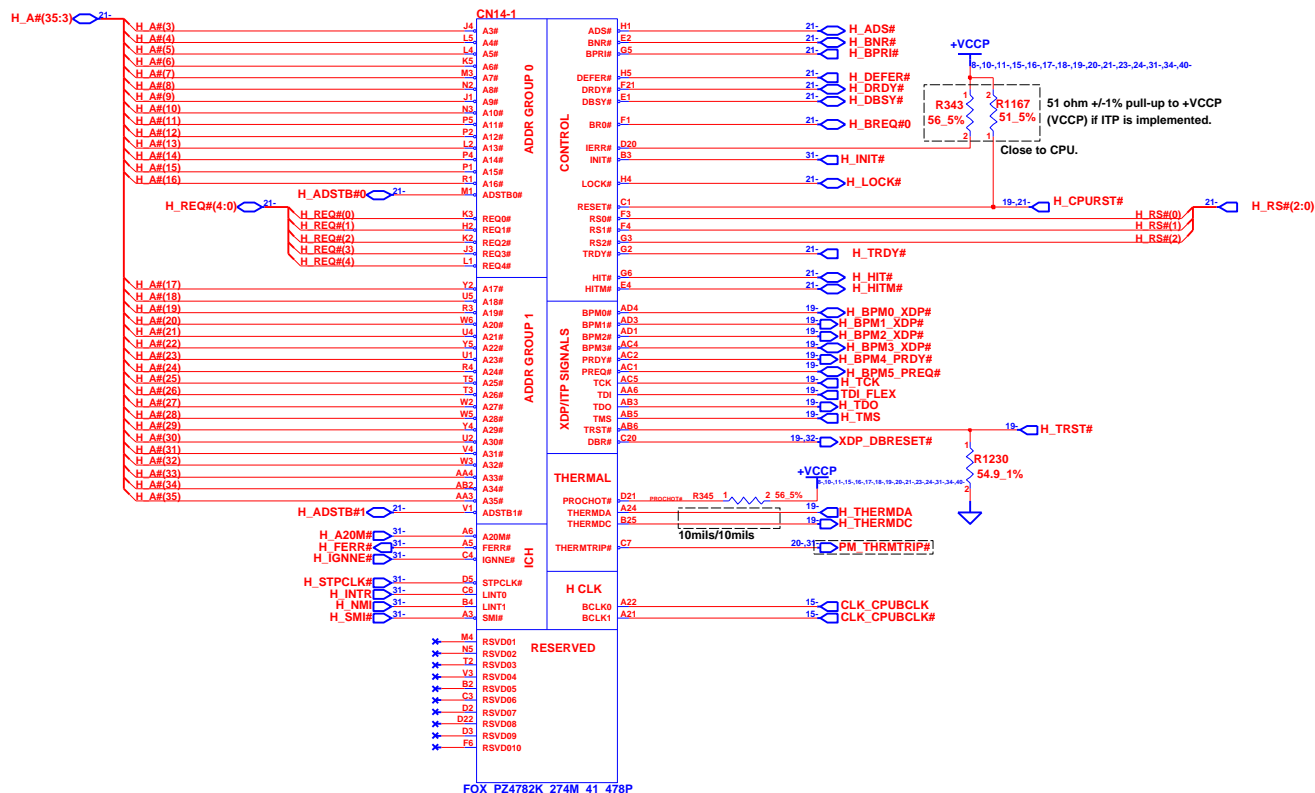
TITLE
**INVICTA Cycle1
Power (Sequence)**

SIZE A3	CODE CS	DOC NUMBER 1310A22527-0-MTR	REV A03
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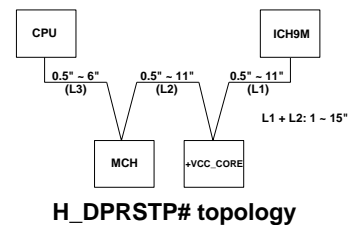
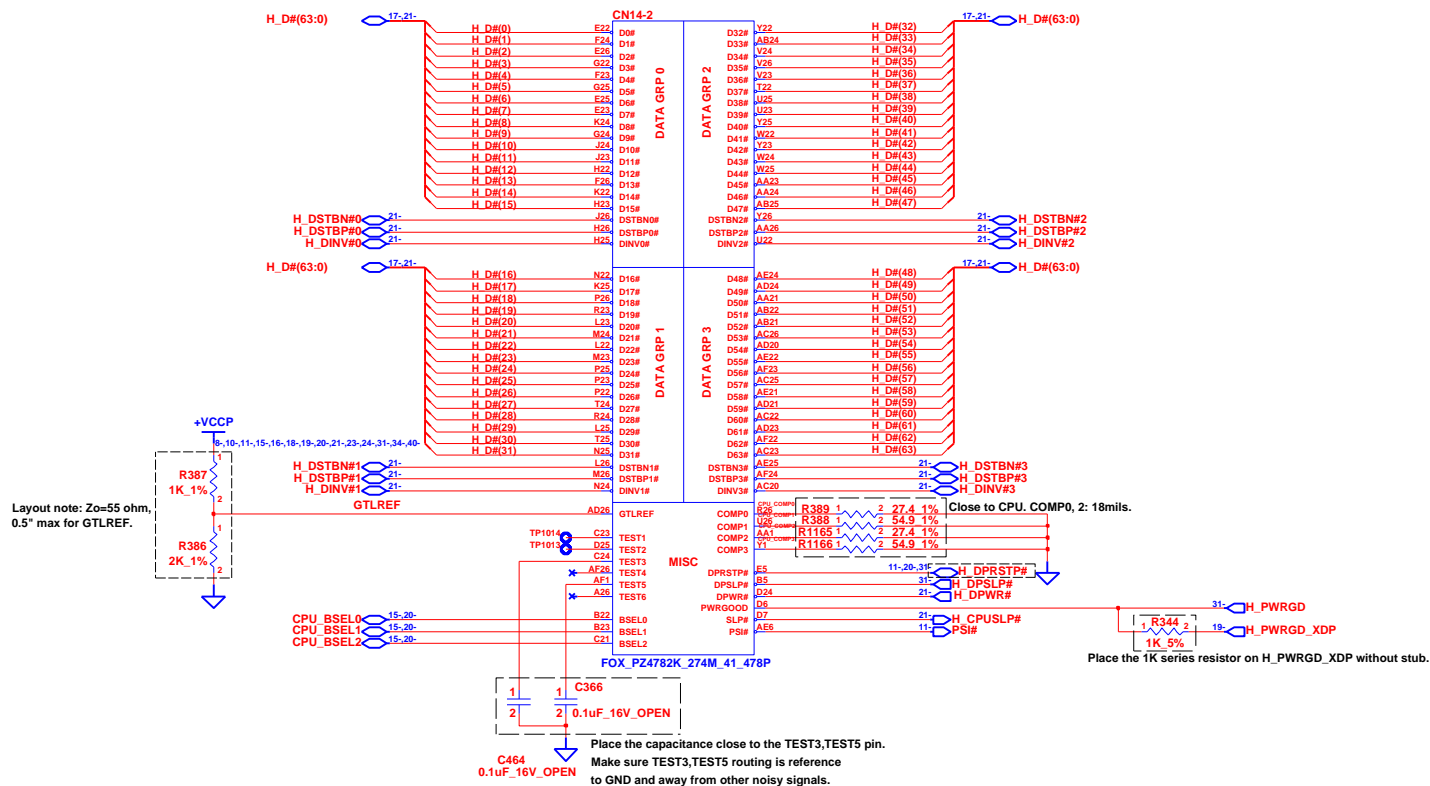
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INVENTEC			
TITLE INVICTA Cycle1 CPU Penryn-1			
SIZE A3	CODE CS	DOC. NUMBER 1310A22527-0-MTR	REV A03
CHANGE by		Drawer Name	17-Mar-2009
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INVENTEC			
TITLE INVICTA Cycle1			
CPU Penryn-2			
SIZE A3	CODE CS	DOC. NUMBER 1310A22527-0-MTR	REV A03
CHANGE by		Drawer Name	17-Mar-2009
SHEET		17	OF 54

(36A) <35W>

+VCC_CORE
11,18,40

+VCC_CORE
11,18,40

PLACE THESE INSIDE SOCKET
CAVITY ON L8 (NORTH SIDE
SECONDARY)

C448 1 10uF_6.3v
C450 1 10uF_6.3v
C449 1 10uF_6.3v
C451 1 10uF_6.3v
C461 1 10uF_6.3v

C456 1 22uF_6.3v
C452 1 10uF_6.3v
C429 1 10uF_6.3v
C432 1 10uF_6.3v
C431 1 10uF_6.3v

PLACE THESE INSIDE SOCKET
CAVITY ON L8 (SOUTH SIDE
SECONDARY)

C434 1 10uF_6.3v
C427 1 22uF_6.3v
C437 1 10uF_6.3v
C436 1 10uF_6.3v
C463 1 10uF_6.3v

C1259 1 10uF_6.3v
C1270 1 10uF_6.3v
C1262 1 10uF_6.3v
C1261 1 10uF_6.3v
C1260 1 10uF_6.3v

PLACE THESE INSIDE SOCKET
CAVITY ON L1 (NORTH SIDE
PRIMARY)

C1264 1 22uF_6.3v
C1263 1 22uF_6.3v
C1266 1 22uF_6.3v
C1265 1 22uF_6.3v
C1269 1 22uF_6.3v
C1274 1 10uF_6.3v

PLACE THESE INSIDE SOCKET
CAVITY ON L1 (SOUTH SIDE
PRIMARY)

C1271 1 22uF_6.3v
C1275 1 10uF_6.3v
C1272 1 22uF_6.3v
C1273 1 22uF_6.3v
C462 1 22uF_6.3v
C1258 1 22uF_6.3v

SOUTH SIDE SECONDARY

C1256 330uF_2v_9mR_Panasonic
C1257 330uF_2v_9mR_Panasonic

NORTH SIDE SECONDARY

C465 330uF_2v_9mR_Panasonic
C466 330uF_2v_9mR_Panasonic

CN14-3
A7 VCC001
A9 VCC002
A19 VCC003
A12 VCC004
A13 VCC005
A15 VCC006
A17 VCC007
A18 VCC008
A20 VCC009
B7 VCC010
B9 VCC011
B10 VCC012
B12 VCC013
B14 VCC014
B15 VCC015
B17 VCC016
B18 VCC017
B20 VCC018
C9 VCC019
C10 VCC020
C12 VCC021
C13 VCC022
C15 VCC023
C17 VCC024
C18 VCC025
D9 VCC026
D10 VCC027
D12 VCC028
D14 VCC029
D15 VCC030
D17 VCC031
D18 VCC032
E7 VCC033
E9 VCC034
E10 VCC035
E12 VCC036
E13 VCC037
E15 VCC038
E17 VCC039
E18 VCC040
E20 VCC041
F7 VCC042
F9 VCC043
F10 VCC044
F12 VCC045
F14 VCC046
F15 VCC047
F17 VCC048
F18 VCC049
F20 VCC050
AA7 VCC051
AA8 VCC052
AA10 VCC053
AA12 VCC054
AA13 VCC055
AA15 VCC056
AA17 VCC057
AA18 VCC058
AA20 VCC059
AB9 VCC060
AB10 VCC061
AB12 VCC062
AB14 VCC063
AB15 VCC064
AB17 VCC065
AB18 VCC066
AB19 VCC067
AB20 VCC068
AB7 VCC069
AC7 VCC070
AC9 VCC071
AC12 VCC072
AC13 VCC073
AC15 VCC074
AC17 VCC075
AC18 VCC076
AD7 VCC077
AD9 VCC078
AD10 VCC079
AD12 VCC080
AD14 VCC081
AD15 VCC082
AD17 VCC083
AD18 VCC084
AE9 VCC085
AE10 VCC086
AE12 VCC087
AE13 VCC088
AE15 VCC089
AE17 VCC090
AE18 VCC091
AE20 VCC092
AF9 VCC093
AF10 VCC094
AF12 VCC095
AF14 VCC096
AF15 VCC097
AF17 VCC098
AF18 VCC099
AF20 VCC100
G21 VCC101
V6 VCC102
J6 VCC103
K6 VCC104
M6 VCC105
J21 VCC106
K21 VCC107
M21 VCC108
N21 VCC109
N6 VCC110
R21 VCC111
R6 VCC112
T21 VCC113
T6 VCC114
V21 VCC115
W21 VCC116
VCCA01
VCCA02
VID0
VID1
VID2
VID3
VID4
VID5
VID6
VCCSENSE
VSSSENSE
FOX_P24782K_274M_41_478P

4.5A - before Vcc stable.
2.5A - after Vcc stable.

+VCCP

C443 1 220uF_2.5v

PLACE THESE INSIDE SOCKET
CAVITY ON L8 (NORTH SIDE
SECONDARY)

C440 1 0.1uF_16v
C441 1 0.1uF_16v
C444 1 0.1uF_16v
C439 1 0.1uF_16v
C442 1 0.1uF_16v
C445 1 0.1uF_16v

(130mA)

+V1.5S

C1312 1 0.01uF_16v
C1469 1 10uF_6.3v

LAYOUT NOTE:
PLACE C1223 NEAR PIN B26

LAYOUT NOTE:
ROUTE VCCSENSE AND VSSSENSE TRACE AT
24.7 OHM WITH 50 MIL SPACING
PLACE PU AND PD WITHIN 1 INCH OF CPU

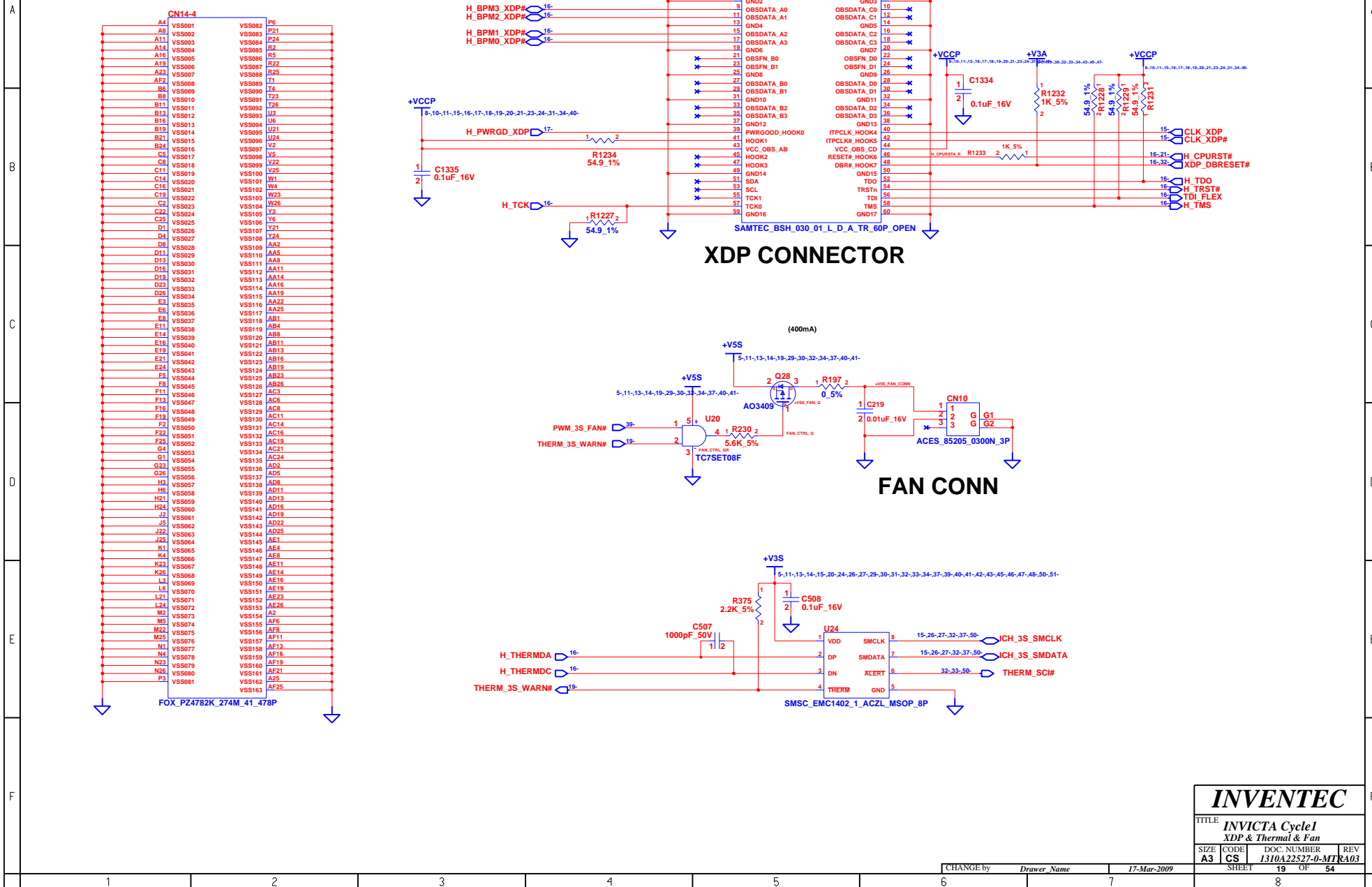
INVENTEC

TITLE
INVICTA Cycle1
CPU Penryn-3

SIZE	CODE	DOC. NUMBER	REV
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CHANGE by Drawer Name 17-Mar-2009

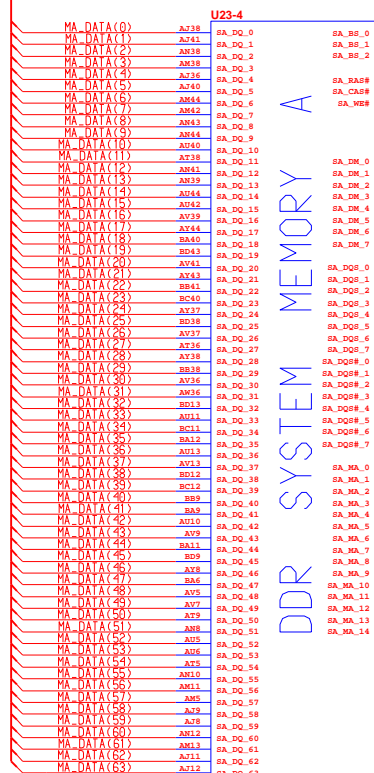
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MA_DATA(63:0) 26

MB_DATA(63:0) 27



ITL_CANTIGA_FCBGA_1329P



ITL_CANTIGA_FCBGA_1329P

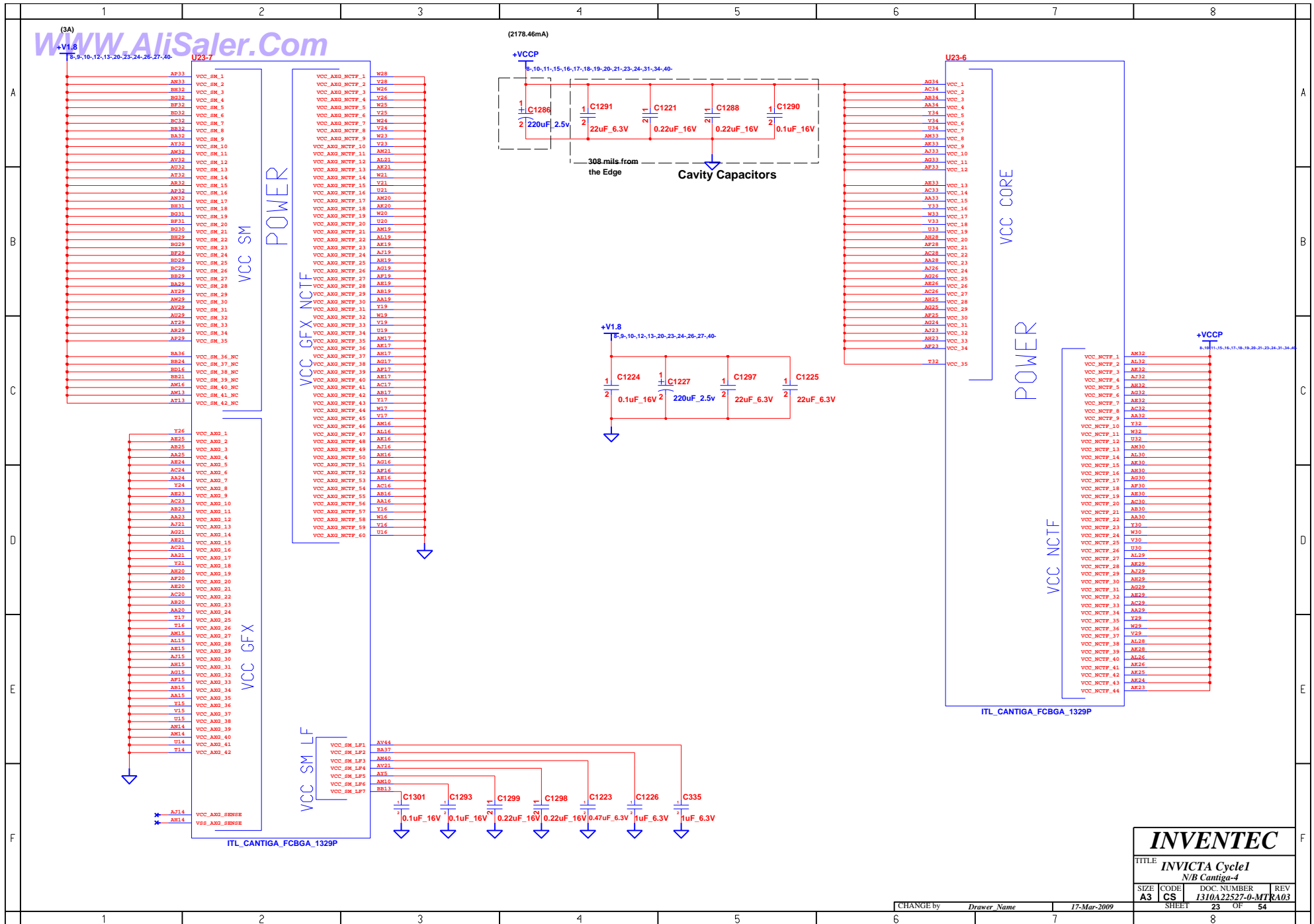
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TITLE
INVICTA Cycle1
N/B Cantiga-3

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22527-0-MTR	A03

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U23-9

AV48	V88_1	AM36
AR48	V88_2	AR36
AL48	V88_3	P36
BR47	V88_4	V46
AM47	V88_5	V46
AM47	V88_6	P36
AZ47	V88_7	R36
AD47	V88_8	AM34
AB47	V88_9	AM35
Y47	V88_10	Y35
Y47	V88_11	V46
Y47	V88_12	V46
Y47	V88_13	BP34
L47	V88_14	AM34
Q47	V88_15	AZ34
BA46	V88_16	AZ34
BA46	V88_17	AM34
AY46	V88_18	V44
AY46	V88_19	R34
AM46	V88_20	A34
AM46	V88_21	RG33
V46	V88_22	BC33
R46	V88_23	AM33
P46	V88_24	AM33
R46	V88_25	V44
P46	V88_26	V44
BP44	V88_27	AM33
AM44	V88_28	AM33
AD44	V88_29	P33
AM44	V88_30	V44
T44	V88_31	R33
DA44	V88_32	R32
T44	V88_33	K32
M44	V88_34	P32
BC43	V88_35	C32
AY43	V88_36	A31
AY43	V88_37	AM29
AM43	V88_38	V29
AM43	V88_39	V29
J43	V88_40	K29
C43	V88_41	V29
RG42	V88_42	V29
AY42	V88_43	A29
AT42	V88_44	RG28
AM42	V88_45	RG28
AT42	V88_46	RG28
AM42	V88_47	RG28
N42	V88_48	V28
L42	V88_49	AM28
BP41	V88_50	AM28
AM41	V88_51	AM28
AM41	V88_52	AM28
AD41	V88_53	AM28
AM41	V88_54	V28
Y41	V88_55	V28
Y41	V88_56	K28
Y41	V88_57	V28
Y41	V88_58	V28
Y41	V88_59	V28
G41	V88_60	BP26
R41	V88_61	AM26
RG40	V88_62	AT26
BP40	V88_63	AM26
AY40	V88_64	AM26
AM40	V88_65	V26
R40	V88_66	V26
R40	V88_67	RG25
AT39	V88_68	RG25
AM39	V88_69	RG25
AT39	V88_70	RG25
AM39	V88_71	RG25
N39	V88_72	AT25
B39	V88_73	AC25
BB38	V88_74	V25
BB38	V88_75	V25
BC38	V88_76	V25
BA38	V88_77	V25
AU38	V88_78	V25
AB38	V88_79	V25
AD38	V88_80	BP24
AA38	V88_81	AT24
Y38	V88_82	AT24
V38	V88_83	AT24
T38	V88_84	AT24
J38	V88_85	AT24
F38	V88_86	AT24
C38	V88_87	AM24
BP37	V88_88	R24
BP37	V88_89	R24
AM37	V88_90	V24
AT37	V88_91	V24
AM37	V88_92	V24
AT37	V88_93	V24
R37	V88_94	R24
C37	V88_95	RG23
BA36	V88_96	AM23
BB36	V88_97	V23
AK35	V88_98	B23
AU36	V88_99	A23

ITL CANTIGA_FCBGA_1329P

U23-10

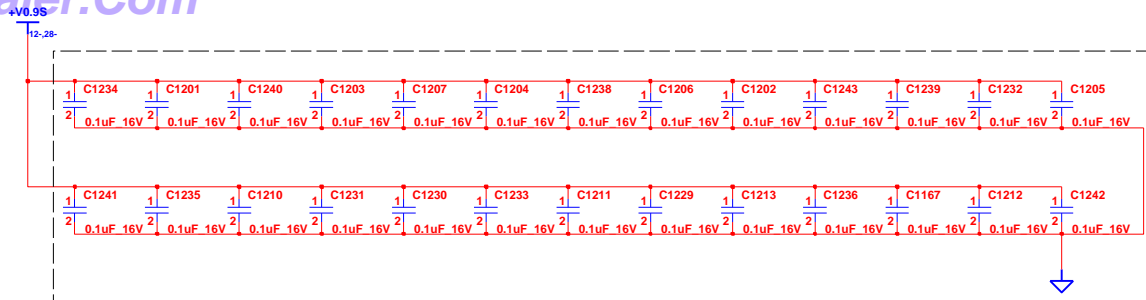
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AM21	V88_204	V88_302
AM21	V88_205	V88_303
AM21	V88_206	V88_304
AM21	V88_207	V88_305
AM21	V88_208	V88_306
AM21	V88_209	V88_307
AM21	V88_210	V88_308
AM21	V88_211	V88_309
AM21	V88_212	V88_310
AM21	V88_213	V88_311
AM21	V88_214	V88_312
AM21	V88_215	V88_313
AM21	V88_216	V88_314
AM21	V88_217	V88_315
AM21	V88_218	V88_316
AM21	V88_219	V88_317
AM21	V88_220	V88_318
AM21	V88_221	V88_319
AM21	V88_222	V88_320
AM21	V88_223	V88_321
AM21	V88_224	V88_322
AM21	V88_225	V88_323
AM21	V88_226	V88_324
AM21	V88_227	V88_325
AM21	V88_228	V88_326
AM21	V88_229	V88_327
AM21	V88_230	V88_328
AM21	V88_231	V88_329
AM21	V88_232	V88_330
AM21	V88_233	V88_331
AM21	V88_234	V88_332
AM21	V88_235	V88_333
AM21	V88_236	V88_334
AM21	V88_237	V88_335
AM21	V88_238	V88_336
AM21	V88_239	V88_337
AM21	V88_240	V88_338
AM21	V88_241	V88_339
AM21	V88_242	V88_340
AM21	V88_243	V88_341
AM21	V88_244	V88_342
AM21	V88_245	V88_343
AM21	V88_246	V88_344
AM21	V88_247	V88_345
AM21	V88_248	V88_346
AM21	V88_249	V88_347
AM21	V88_250	V88_348
AM21	V88_251	V88_349
AM21	V88_252	V88_350
AM21	V88_253	V88_351
AM21	V88_254	V88_352
AM21	V88_255	V88_353
AM21	V88_256	V88_354
AM21	V88_257	V88_355
AM21	V88_258	V88_356
AM21	V88_259	V88_357
AM21	V88_260	V88_358
AM21	V88_261	V88_359
AM21	V88_262	V88_360
AM21	V88_263	V88_361
AM21	V88_264	V88_362
AM21	V88_265	V88_363
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AM21	V88_267	V88_365
AM21	V88_268	V88_366
AM21	V88_269	V88_367
AM21	V88_270	V88_368
AM21	V88_271	V88_369
AM21	V88_272	V88_370
AM21	V88_273	V88_371
AM21	V88_274	V88_372
AM21	V88_275	V88_373
AM21	V88_276	V88_374
AM21	V88_277	V88_375
AM21	V88_278	V88_376
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AM21	V88_280	V88_378
AM21	V88_281	V88_379
AM21	V88_282	V88_380
AM21	V88_283	V88_381
AM21	V88_284	V88_382
AM21	V88_285	V88_383
AM21	V88_286	V88_384
AM21	V88_287	V88_385
AM21	V88_288	V88_386
AM21	V88_289	V88_387
AM21	V88_290	V88_388
AM21	V88_291	V88_389
AM21	V88_292	V88_390
AM21	V88_293	V88_391
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AM21	V88_296	V88_394

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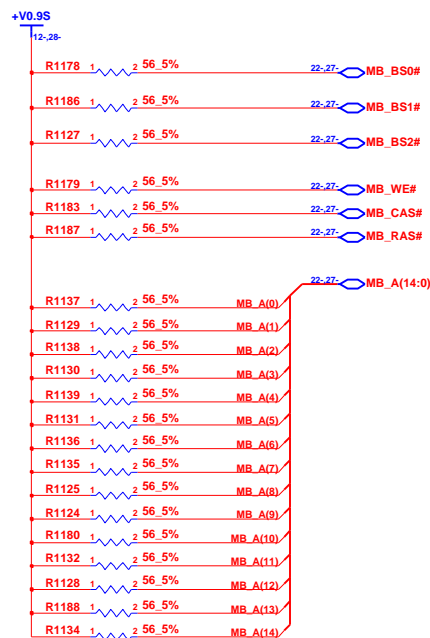
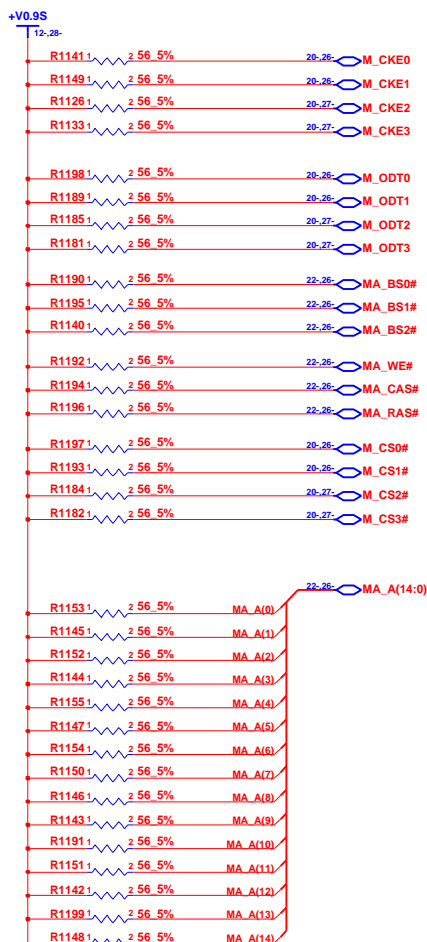
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TITLE: INVICTA Cycle1			
N/B Cantiga-6			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22527-0-MTR	A03
SHEET		OF	
25		54	

SO DIMM0_9.2mm

TITLE			
INVICTA Cycle1 DDR2 DIMMO			
SIZE	CODE	DOC. NUMBER	
A3	CS	1310A22527-0-MTRA03	
SHEET		26	OF 54

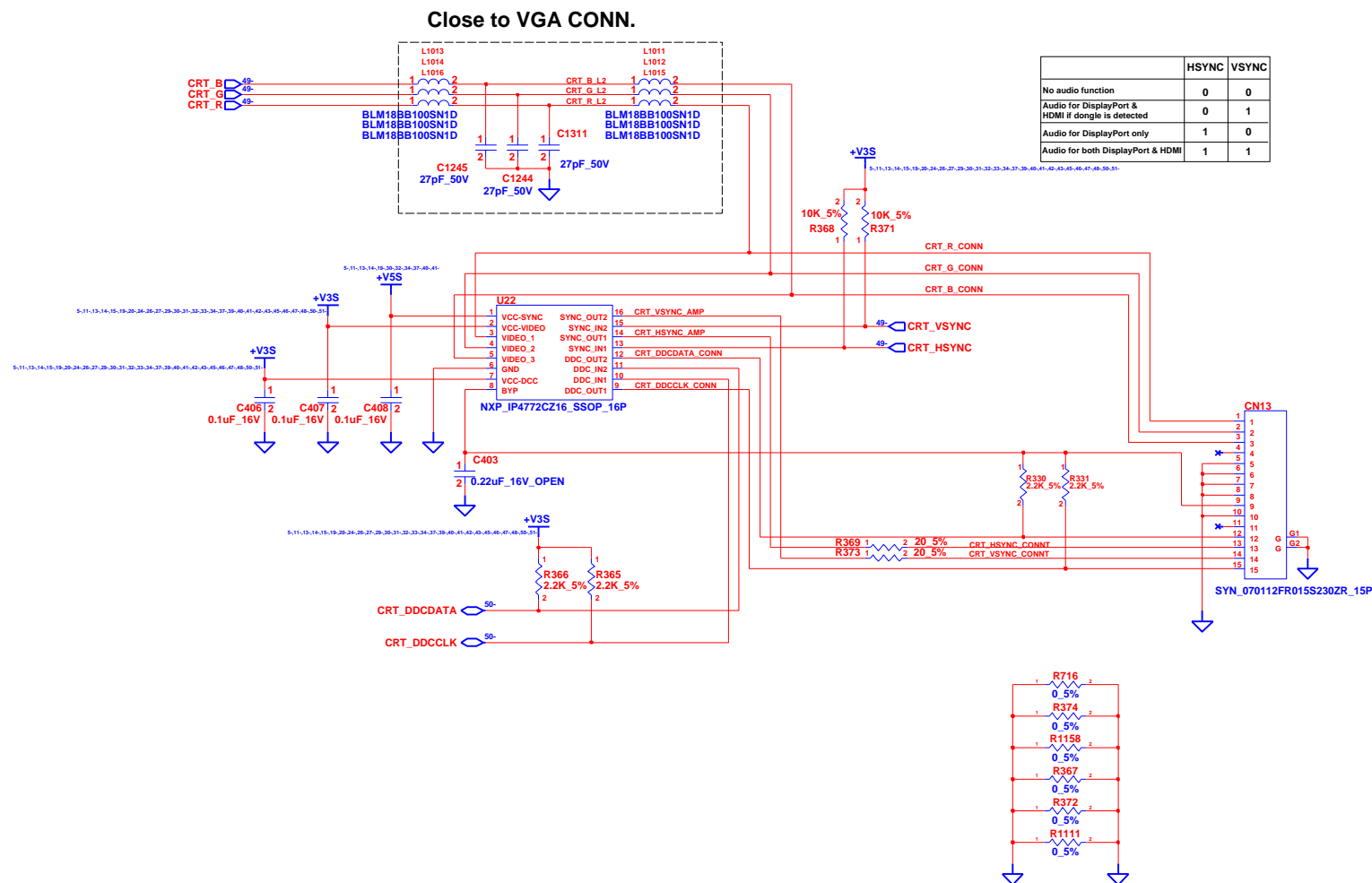


Layout notes : Place one Cap close to every 2 pull up resistor terminated to +V0.9S.



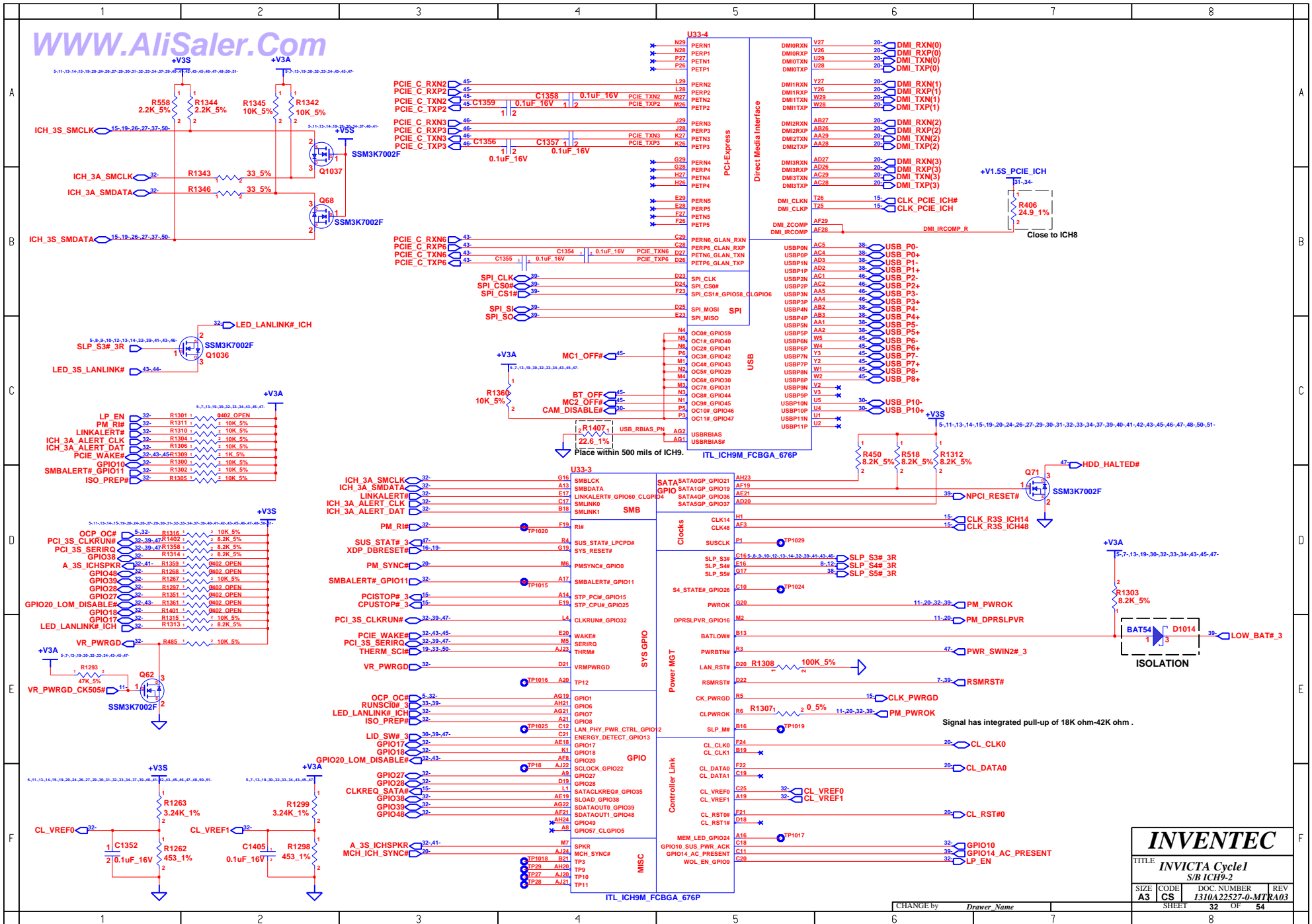
INVENTEC			
TITLE INVICTA Cycle1 DDR2 termination			
SIZE A3	CODE CS	DOC. NUMBER I310A22527-0-MTR	REV A03
SHEET 28		OF 54	

CHANGE by *Drawn Name* 27-Sep-2003



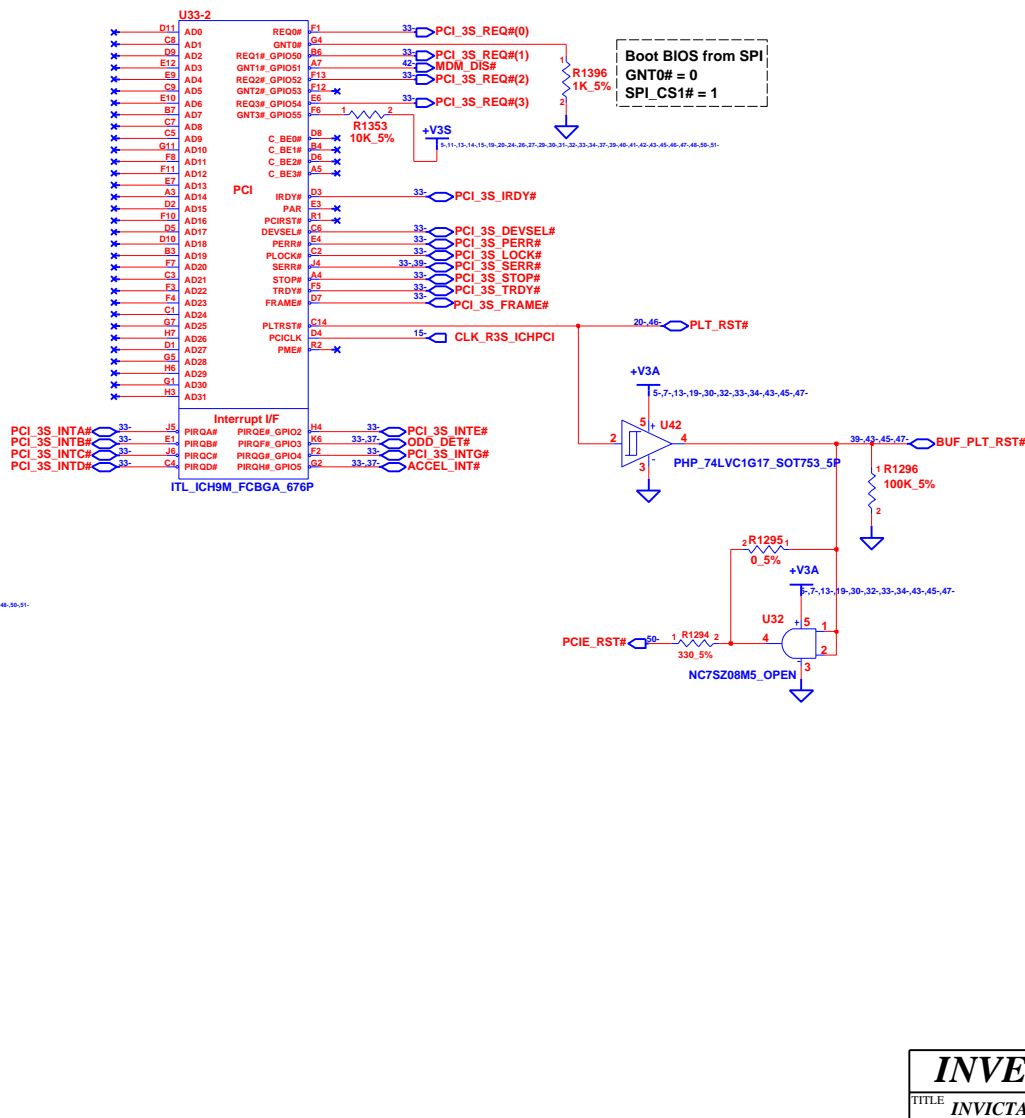
INVENTEC			
TITLE INVICTA Cycle1 VGA CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22527-0-MTR	REV A03
CHANGE by		Drawer Name	17-Mar-2009
SHEET		29	OF 54





INVITEC

TITLE: INVICTA Cycle1 S/B ICH9-2			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22527-0-MTR	REV: A03
SHEET: 32		OF: 54	



INVENTEC

TITLE
INVICTA Cycle1
S/B ICH9-3

SIZE A3	CODE CS	DOC. NUMBER I310A22527-0-MTR	REV A03
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CHANGE by: Drawer Name 17-Mar-2009

SHEET 33 OF 54

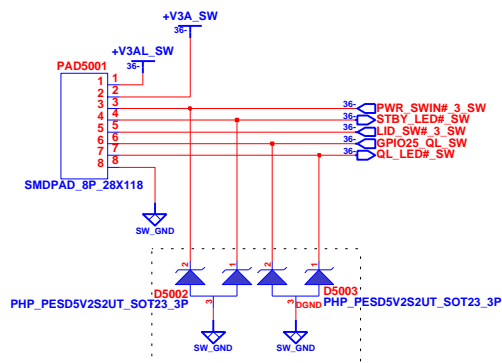
	1	2	3	4	5	6	7	8									
A	WWW.AliSaler.Com				<div>U33-5</div> <div><div><div>AA26</div><div>AA27</div><div>AA3</div><div>AA6</div><div>AB1</div><div>AA23</div><div>AB28</div><div>AB29</div><div>AB4</div><div>AB5</div><div>AC17</div><div>AC26</div><div>AC27</div><div>AC3</div><div>AD1</div><div>AD10</div><div>AD12</div><div>AD13</div><div>AD14</div><div>AD17</div><div>AD18</div><div>AD21</div><div>AD28</div><div>AD29</div><div>AD4</div><div>AD5</div><div>AD6</div><div>AD7</div><div>AD9</div><div>AE12</div><div>AE13</div><div>AE14</div><div>AE16</div><div>AE17</div><div>AE2</div><div>AE20</div><div>AE24</div><div>AE3</div><div>AE4</div><div>AE6</div><div>AE9</div><div>AF13</div><div>AF16</div><div>AF18</div><div>AF22</div><div>AH26</div><div>AF26</div><div>AF27</div><div>AF5</div><div>AF7</div><div>AF9</div><div>AG13</div><div>AG16</div><div>AG18</div><div>AG20</div><div>AG23</div><div>AG3</div><div>AG6</div><div>AG9</div><div>AH12</div><div>AH14</div><div>AH17</div><div>AH19</div><div>AH2</div><div>AH22</div><div>AH25</div><div>AH28</div><div>AH5</div><div>AH8</div><div>AJ12</div><div>AJ14</div><div>AJ17</div><div>AJ8</div><div>B11</div><div>B14</div><div>B17</div><div>B2</div><div>B20</div><div>B23</div><div>B6</div><div>B8</div><div>C26</div><div>C27</div><div>E11</div><div>E14</div><div>E18</div><div>E2</div><div>E21</div><div>E24</div><div>E5</div><div>E8</div><div>F18</div><div>F28</div><div>F29</div><div>G12</div><div>G14</div><div>G18</div><div>G21</div><div>G24</div><div>G26</div><div>G27</div><div>G8</div><div>H2</div><div>H23</div><div>H28</div><div>H29</div></div><div><div>VSS001</div><div>VSS002</div><div>VSS003</div><div>VSS004</div><div>VSS005</div><div>VSS006</div><div>VSS007</div><div>VSS008</div><div>VSS009</div><div>VSS010</div><div>VSS011</div><div>VSS012</div><div>VSS013</div><div>VSS014</div><div>VSS015</div><div>VSS016</div><div>VSS017</div><div>VSS018</div><div>VSS019</div><div>VSS020</div><div>VSS021</div><div>VSS022</div><div>VSS023</div><div>VSS024</div><div>VSS025</div><div>VSS026</div><div>VSS027</div><div>VSS028</div><div>VSS029</div><div>VSS030</div><div>VSS031</div><div>VSS032</div><div>VSS033</div><div>VSS034</div><div>VSS035</div><div>VSS036</div><div>VSS037</div><div>VSS038</div><div>VSS039</div><div>VSS040</div><div>VSS041</div><div>VSS042</div><div>VSS043</div><div>VSS044</div><div>VSS045</div><div>VSS046</div><div>VSS047</div><div>VSS048</div><div>VSS049</div><div>VSS050</div><div>VSS051</div><div>VSS052</div><div>VSS053</div><div>VSS054</div><div>VSS055</div><div>VSS056</div><div>VSS057</div><div>VSS058</div><div>VSS059</div><div>VSS060</div><div>VSS061</div><div>VSS062</div><div>VSS063</div><div>VSS064</div><div>VSS065</div><div>VSS066</div><div>VSS067</div><div>VSS068</div><div>VSS069</div><div>VSS070</div><div>VSS071</div><div>VSS072</div><div>VSS073</div><div>VSS074</div><div>VSS075</div><div>VSS076</div><div>VSS077</div><div>VSS078</div><div>VSS079</div><div>VSS080</div><div>VSS081</div><div>VSS082</div><div>VSS083</div><div>VSS084</div><div>VSS085</div><div>VSS086</div><div>VSS087</div><div>VSS088</div><div>VSS089</div><div>VSS090</div><div>VSS091</div><div>VSS092</div><div>VSS093</div><div>VSS094</div><div>VSS095</div><div>VSS096</div><div>VSS097</div><div>VSS098</div><div>VSS099</div><div>VSS100</div><div>VSS101</div><div>VSS102</div><div>VSS103</div><div>VSS104</div><div>VSS105</div><div>VSS106</div></div><div><div>H5</div><div>J23</div><div>J26</div><div>J27</div><div>AC22</div><div>K28</div><div>K29</div><div>L13</div><div>L15</div><div>L2</div><div>L26</div><div>L27</div><div>L5</div><div>L7</div><div>M12</div><div>M13</div><div>M14</div><div>M15</div><div>M16</div><div>M17</div><div>M23</div><div>M28</div><div>M29</div><div>N11</div><div>N12</div><div>N13</div><div>N14</div><div>N15</div><div>N16</div><div>N17</div><div>N18</div><div>N26</div><div>N27</div><div>P12</div><div>P13</div><div>P14</div><div>P15</div><div>P16</div><div>P17</div><div>P2</div><div>P23</div><div>P28</div><div>P29</div><div>P4</div><div>P7</div><div>R11</div><div>R12</div><div>R13</div><div>R14</div><div>R15</div><div>R16</div><div>R17</div><div>R18</div><div>R28</div><div>T12</div><div>T13</div><div>T14</div><div>T15</div><div>T16</div><div>T17</div><div>T23</div><div>R28</div><div>U12</div><div>U13</div><div>U14</div><div>U15</div><div>U16</div><div>U17</div><div>AD23</div><div>U26</div><div>U27</div><div>U3</div><div>V1</div><div>V13</div><div>V15</div><div>V23</div><div>V28</div><div>V29</div><div>V4</div><div>V5</div><div>W23</div><div>W27</div><div>W3</div><div>Y1</div><div>Y28</div><div>Y29</div><div>Y4</div><div>Y5</div><div>AG28</div><div>AH6</div><div>AF2</div><div>B25</div></div></div> <div><div>48</div><div>48</div><div>48</div><div>48</div></div> <div><div>ICHGND1</div><div>ICHGND4</div><div>ICHGND7</div><div>ICHGND10</div></div> <div><div>ITL_ICH9M_FCBGA_676P</div></div>												
B																	
C																	
D																	
E																	
F																	
	1	2	3	4	5	6	7	8									

INVENTEC			
TITLE INVICTA Cycle1			
S/B ICH9-5			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22527-0-MT	RA03
SHEET		35	OF 54

CHANGE by	Drawer Name	17-Mar-2009
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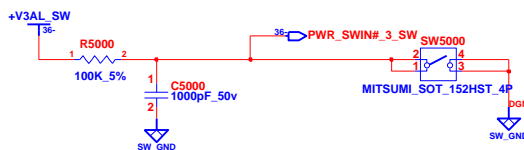
1	2	3	4	5	6	7	8
---	---	---	---	---	---	---	---

PWR SW B/B CONN

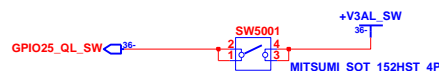


For ESD

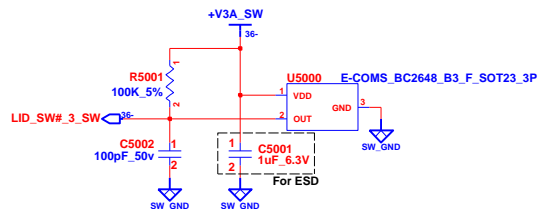
Power Button



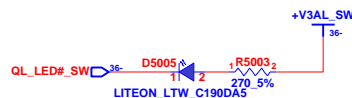
Quick Look Button



LID Switch



Quick Look LED



Power / Standby LED



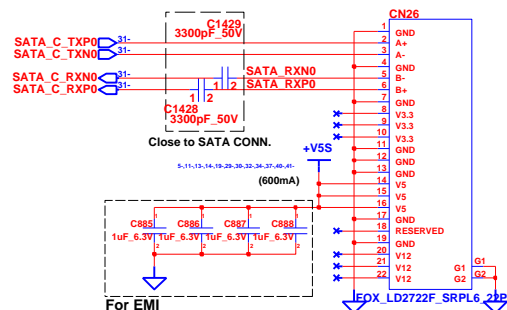
- FIX5000
- FIX_MASK
- FIX5001
- FIX_MASK
- FIX5002
- FIX_MASK
- FIX5003
- FIX_MASK
- FIX5004
- FIX_MASK
- FIX5005
- FIX_MASK

Power & LID Switch Board

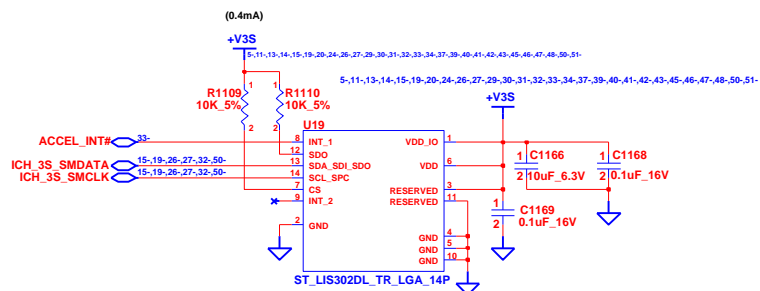
INVENTEC

TITLE			INVENTEC Cycle1	
			PWR, LID, APP SW/Bs	
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	1310A22527-0-MTR	A03	
SHEET		36	OF	54

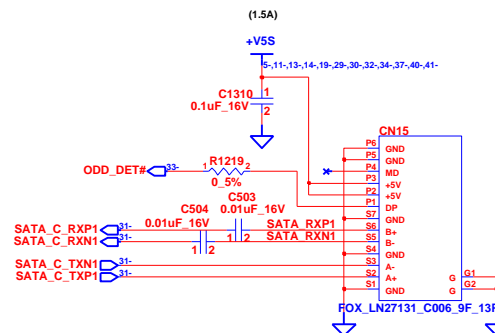
CHANGE by: Drawer Name 17-Mar-2009



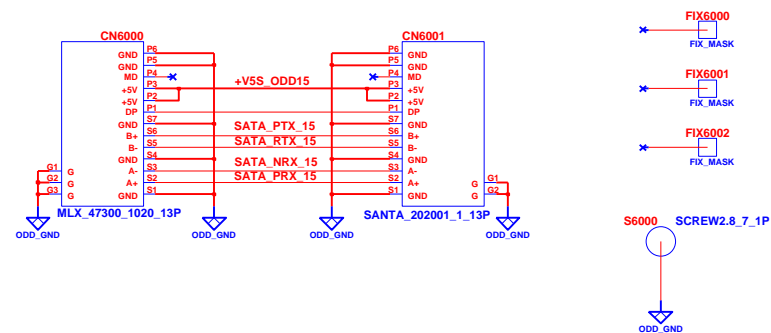
SATA HDD



Accelerometer



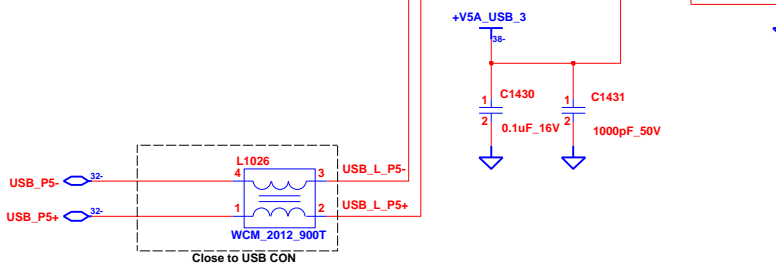
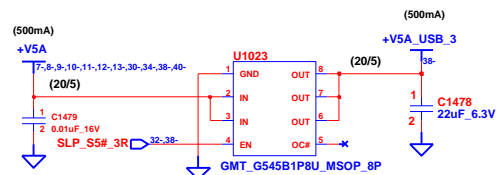
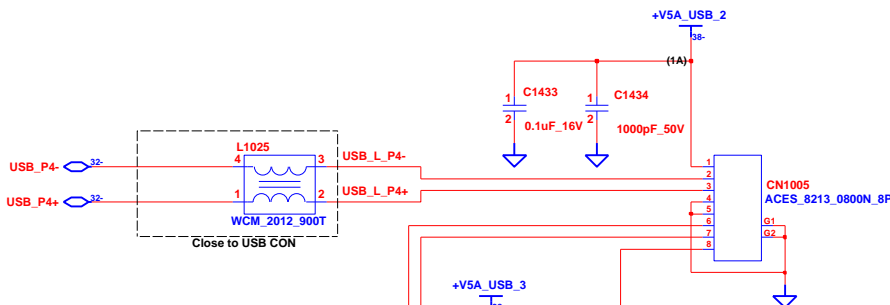
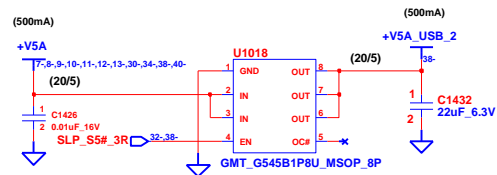
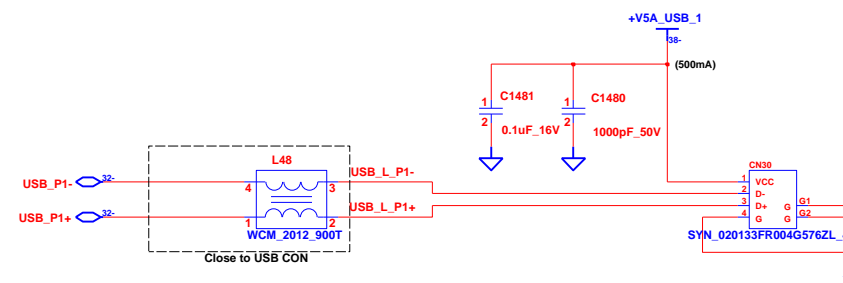
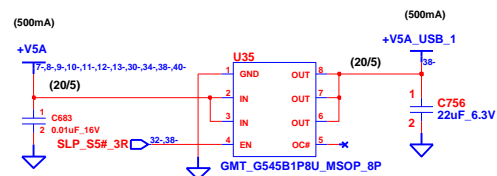
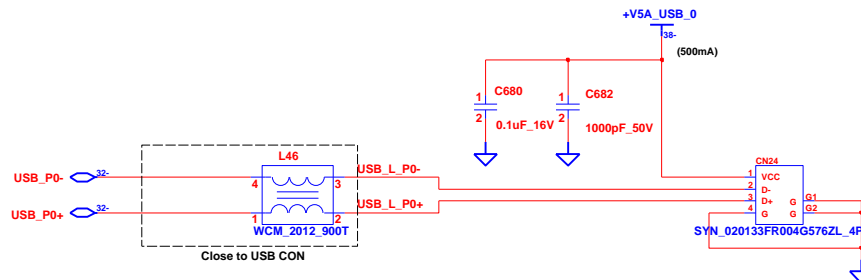
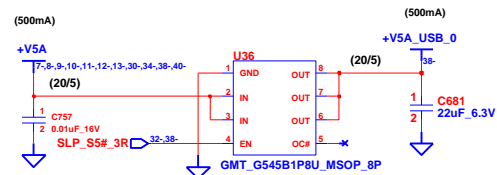
SATA ODD



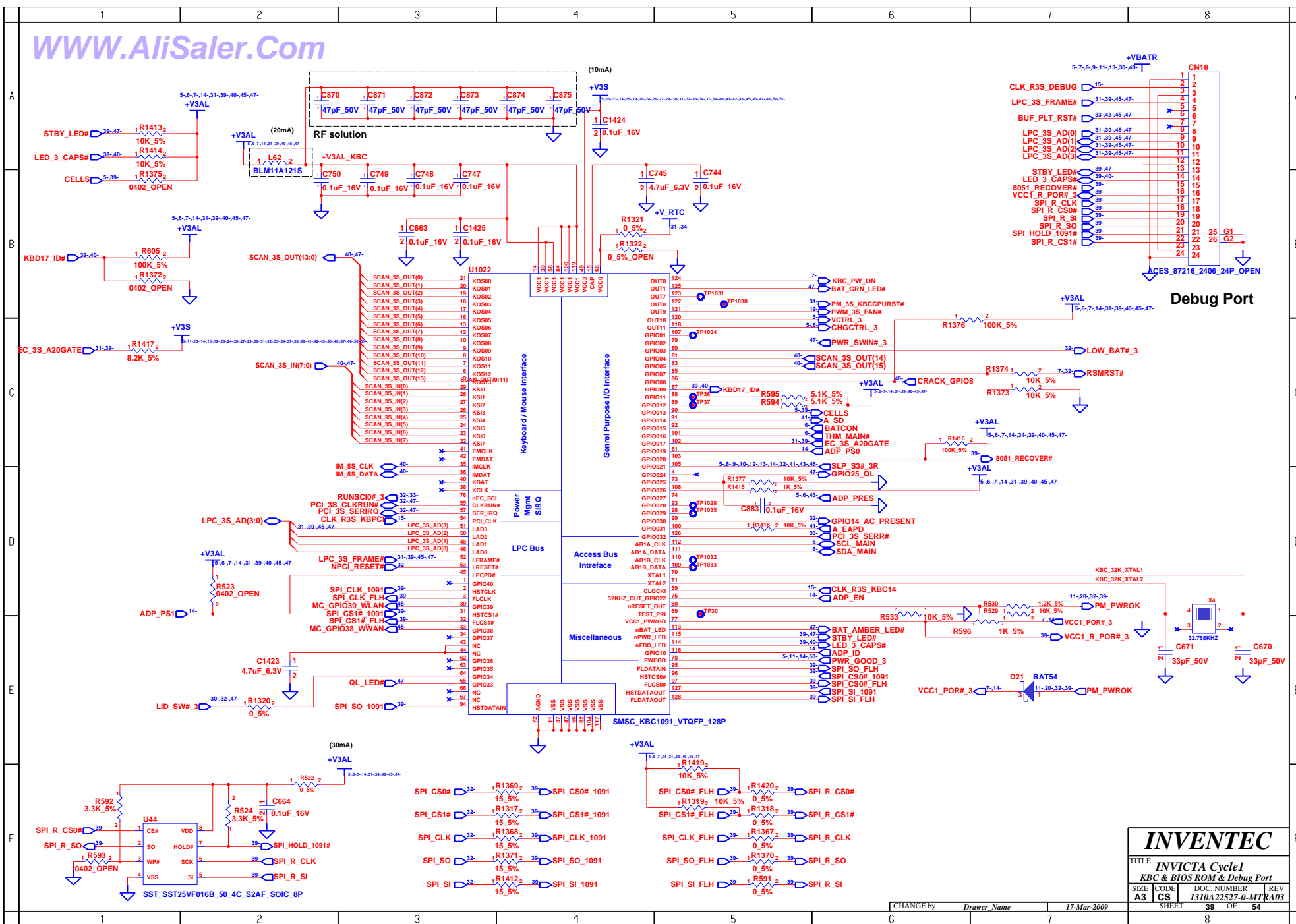
15" ODD Extend Board

INVENTEC

TITLE INVICTA Cycle1			
HDD & ODD CONN & Accelerometer & 15" ODD EXT/B			
SIZE A3	CODE CS	DOC NUMBER I310A22527-0-MTR	REV A03
CHANGE by Drawer Name		17-Mar-2009	
SHEET		37 OF 54	



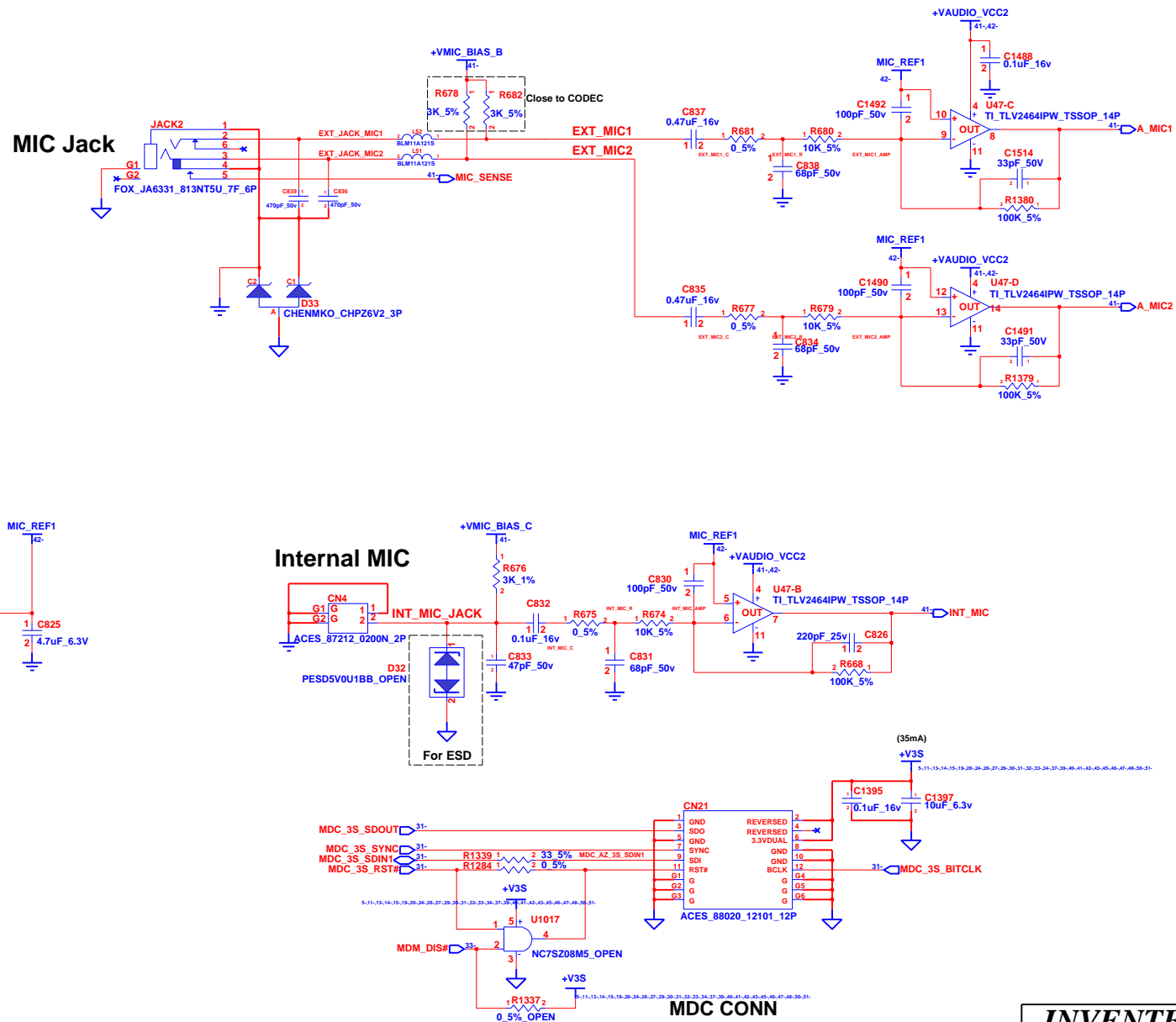
INVENTEC			
TITLE: INVICTA Cycle1 USB CONN			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A22527-0-MTR	REV: A03
CHANGE by: Drawer Name		17-Mar-2009	
SHEET: 38		OF: 54	



INVENTEC

TITLE: INVICTA Cycle1			
KBC & BIOS ROM & Debug Port			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A22527-0-MTR	REV: A03
SHEET: 39		OF: 54	

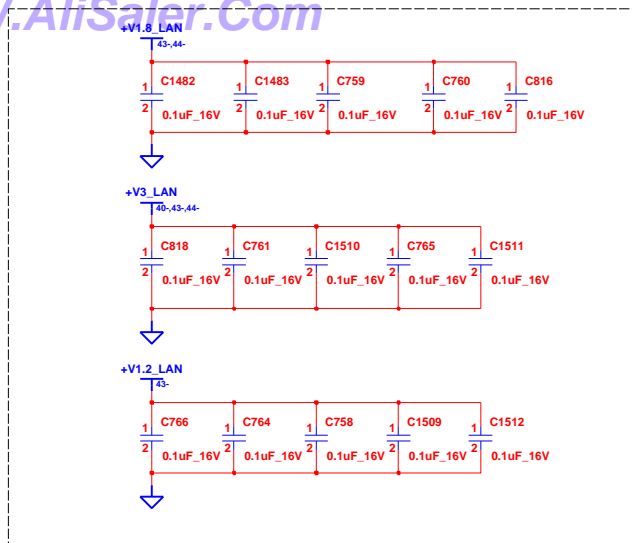




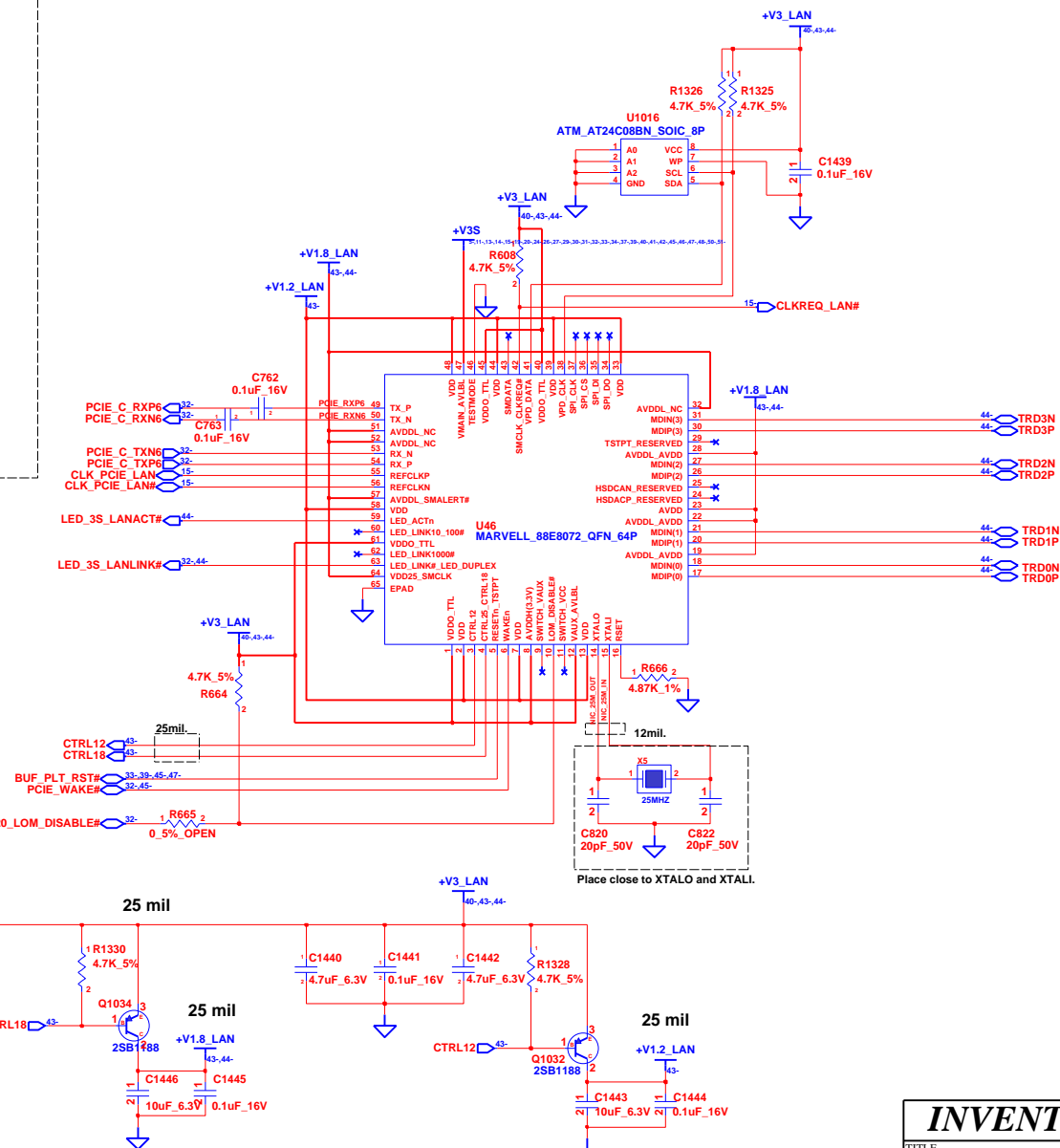
INVENTEC

TITLE: INVICTA Cycle1			
Audio MIC & MDC CONN			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22527-0-MTR	REV: A03
SHEET: 42		OF: 54	

CHANGE by: Drawer Name 17-Mar-2009

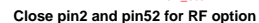
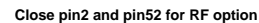


Place bypass cap as close as possible with every power pin.



Place close to XTALO and XTALI.

INVENTEC			
TITLE: INVICTA Cycle1 Giga LAN Controller			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A22527-0-MTR	REV: A03
SHEET: 43		OF: 54	

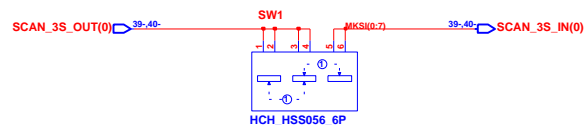


TITLE	<i>INVICTA Cycle1</i>
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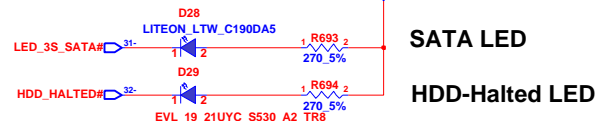
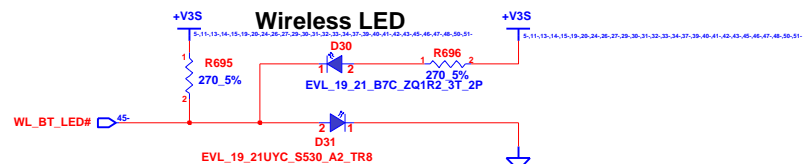
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22527-0-MTRA03	

SHEET 45 OF 54

Wireless Button



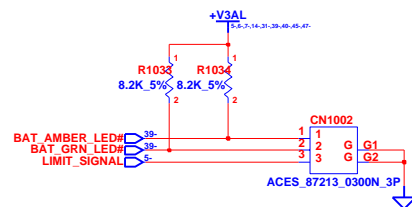
Wireless LED



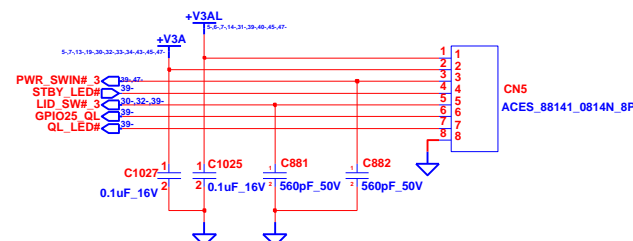
SATA LED

HDD-Halted LED

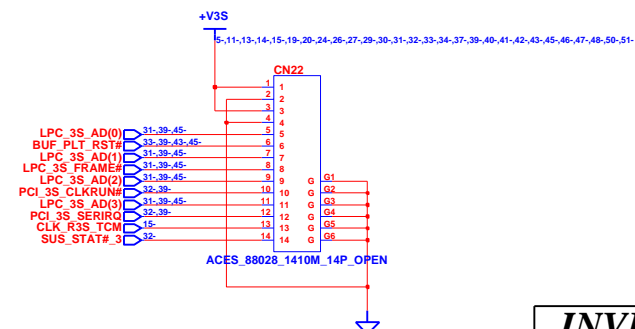
Battery Charge LED CONN



LED & SW B/B CONN



TCM CONN



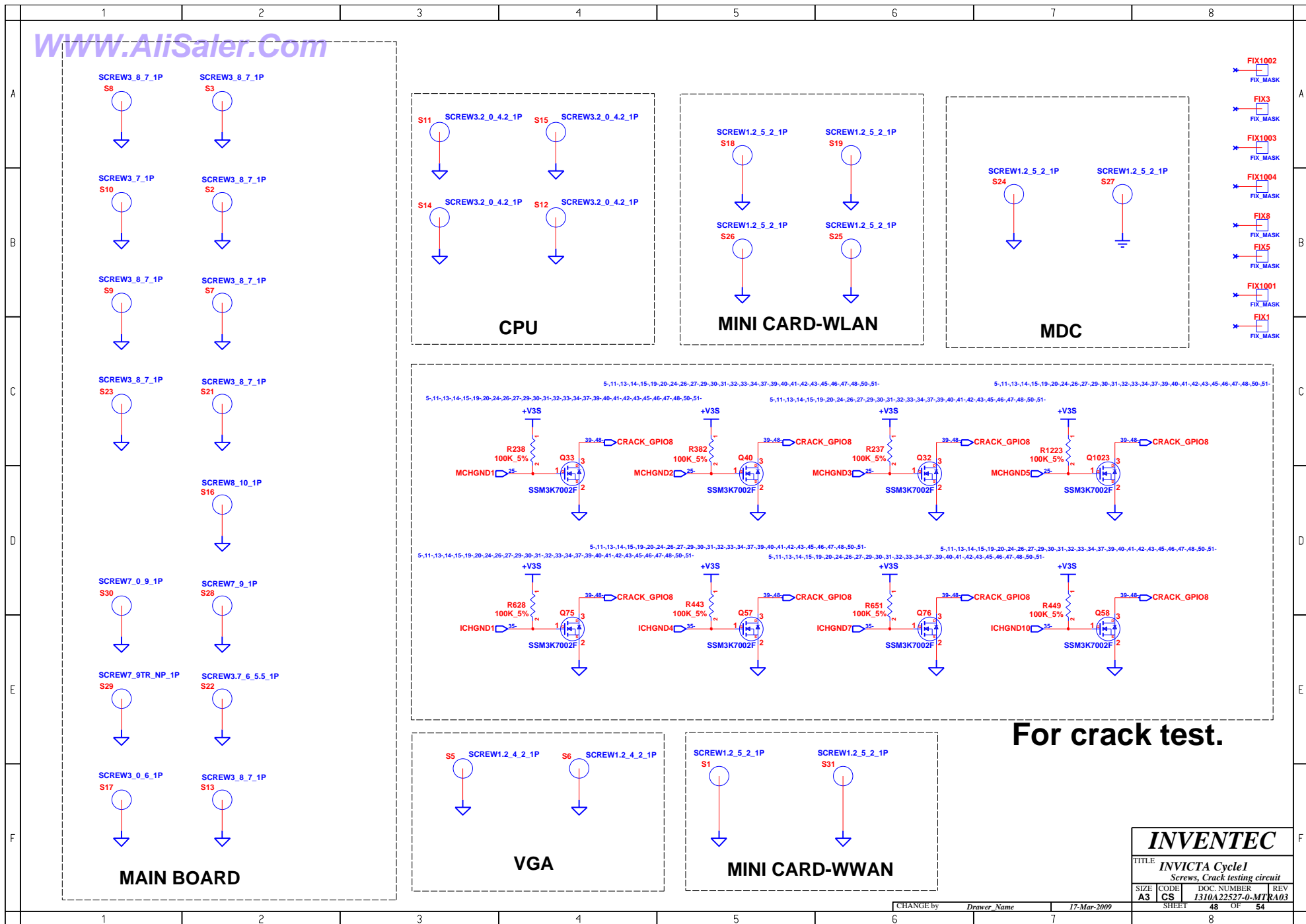
INVENTEC

TITLE
INVICTA Cycle1
LEDs & TCM CONN

SIZE CODE DOC NUMBER REV
A3 CS 1310A22527-0-MTR A03

CHANGE by *Drawer Name* 17-Mar-2009

SHEET 47 OF 54





VM_ADA(63:0) 53-54

VM_ADA(0)
VM_ADA(1)
VM_ADA(2)
VM_ADA(3)
VM_ADA(4)
VM_ADA(5)
VM_ADA(6)
VM_ADA(7)
VM_ADA(8)
VM_ADA(9)
VM_ADA(10)
VM_ADA(11)
VM_ADA(12)
VM_ADA(13)
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VM_ADA(56)
VM_ADA(57)
VM_ADA(58)
VM_ADA(59)
VM_ADA(60)
VM_ADA(61)
VM_ADA(62)
VM_ADA(63)

U15-8

VM_A A BA2
VM_A A BA0
VM_A A BA1

MEMORY INTERFACE

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

VM_A A BA2
VM_A A BA0
VM_A A BA1

+V1.8S

13-40-49-50-51-52-53-54

1 R1097

100_1%

1 C1154

0.1uF_16V

+V1.8S

13-40-49-50-51-52-53-54

1 R1096

100_1%

1 R1094

100_1%

1 C1153

0.1uF_16V

1 R1058

4.7K_5%

1 R1057

4.7K_5%

1 R1059

243_1%

1 TP1001

4.7K_5%

1 R1058

4.7K_5%

1 R1057

4.7K_5%

1 R1059

243_1%

1 TP1001

4.7K_5%

ATI_M92_S2_BGA_631P

DDR_CLKA1

52-54

56_5%

1 R1025

56_5%

1 R1026

56_5%

1 R207

56_5%

1 R209

56_5%

1 C1035

470pF_50V

1 C286

470pF_50V

Layout Note: Put these resistors close to V-RAM.

U15-4

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

VM_A A BA2

VM_A A BA0

VM_A A BA1

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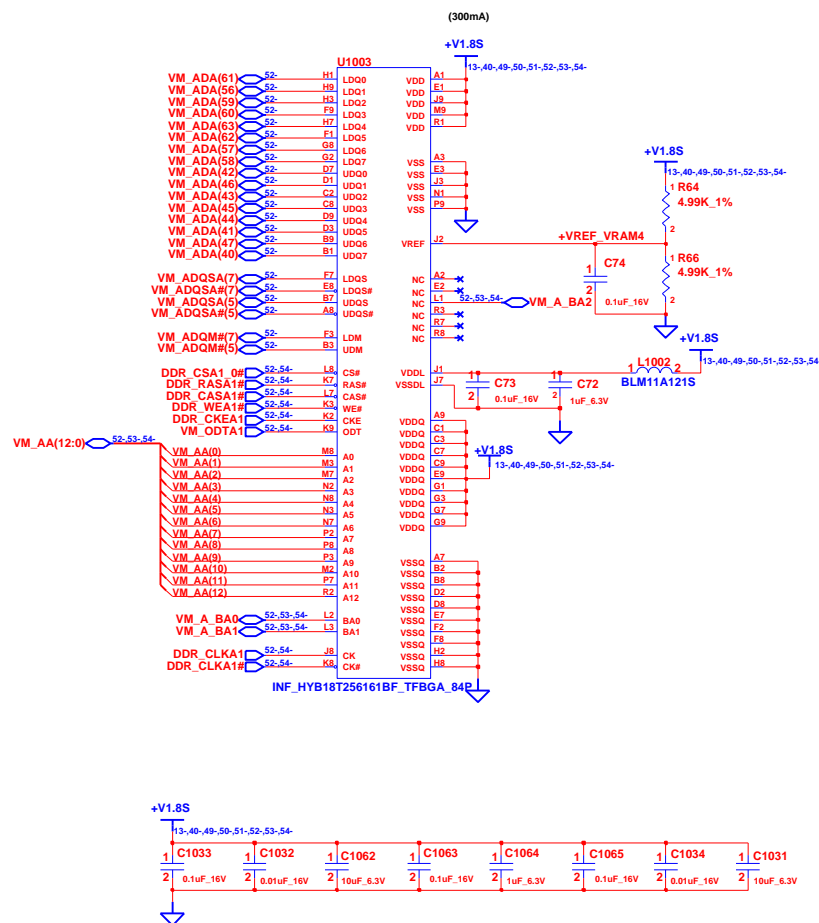
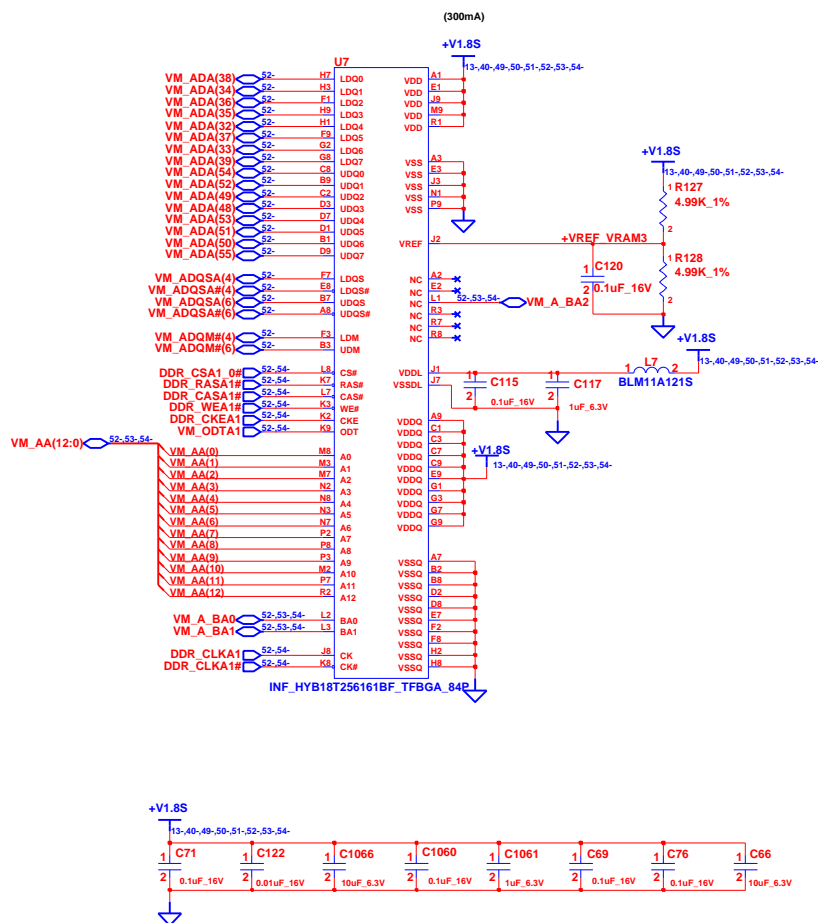
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INVENTEC

TITLE			
INVICTA Cycle1			
Video RAM (DDR2) -2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22527-0-MTR	A03
SHEET		54	OF 54

CHANGE by Drawer Name 17-Mar-2009